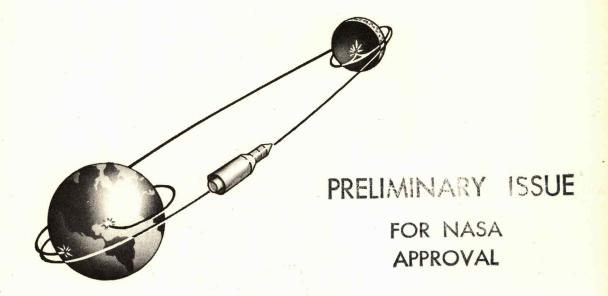
INTERFACE

65ICD9304

OCT - 7 1970



MSFC/KSC
INTERFACE CONTROL DOCUMENT
DEFINITION OF ELECTRICAL INTERFACE
SATURN IB

100 DAY TYPE I
COUNT CLOCK SYSTEM/KSC TIMING

PREPARED BY:

EFFECTIVE ON: LC-39/AS-206 & SUBSEQUENT

GENERAL ELECTRIC COMPANY APOLLO SYSTEMS HUNTSVILLE, ALABAMA

PREPARED FOR:

ELECTRICAL SYSTEMS BRANCH SYSTEMS DIVISION ASTRIONICS LABORATORY MARSHALL SPACE FLIGHT CENTER

NAS8-25155 LINE ITEM NO. DRL 192

National Aeronautics and Space Administration



EFFECTIVE ON:

LC-39/AS-206 &

SUBSEQUENT

MSFC/KSC

INTERFACE CONTROL DOCUMENT

65TCD9304

DEFINITION OF ELECTRICAL INTERFACE

SATURN IB 100 DAY TYPE I

COUNT CLOCK SYSTEM TO

KSC TIMING

011111	MSC		KSC		MSFC		
SYM	NAME	DATE	NAME	DATE	NAME	DATE	
ORIG.							
A							
В							
C							
D							

APPROVAL

REPOSITORY:

THE GEORGE C. MARSHALL SPACE FLIGHT CENTER HUNTSVILLE, ALABAMA

NATIONAL AERONAUTICS AND SPACE ADMINISTRATION

PREPARED FOR:

LAUNCH OPERATIONS ELECTRICAL SUB-PANEL

PANEL

THIS DOCUMENT SHALL NOT BE USED FOR MANUFACTURING, PROCUREMENT OF HARDWARE, CHECKING OF MANUFACTURED ITEMS OR ASSEMBLY BUT SHALL GOVERN PERTINENT DESIGN DOCUMENTATION (CLASS I & II DRAWINGS, ETC.) REVISIONS TO THIS DOCUMENT OR THE PROPERLY IDENTIFIED PERTINENT DESIGN DOCUMENTATION CAN ONLY BE MADE WITH APPROVAL OF THE RESPONSIBLE INTERFACE AUTHORITY.

APOLLO INTER-CENTER INTERFACE CONTROL DOCUMENT

LC-39/AS-206 & SUBSEQUENT

MSFC/KSC

INTERFACE CONTROL DOCUMENT

65ICD9304

DEFINITION OF ELECTRICAL INTERFACE

SATURN IB 100 DAY TYPE I

COUNT CLOCK SYSTEM TO

KSC TIMING

NOTE: SIGNATURES ON THIS SHEET SIGNIFY TECHNICAL CONCURRENCE ONLY AND DO NOT CONSTITUTE DOCUMENT APPROVAL.

C								_	
A ORIG	M.a. Langston with	10/5/10	L.a.V. Mate	10/6/20					
SYM	GENERAL ELECTRIC GENERAL ELECTRIC DESIGN				NASA CONCURRENCE		NASA CONCURRENCE		
1	NAME	DATE	NAME	DATE	NAME		DATE	NAME	DATE

PREPARED BY:

GENERAL ELECTRIC COMPANY
APOLLO SYSTEMS, HUNTSVILLE PROGRAMS
HUNTSVILLE, ALABAMA

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INTERFACE CONTROL DOCUMENT

TABLE OF CONTENTS

	APPROVA	L PAGE			PAGE	I	
	CONCURR	ENCE PAGE			PAGE	II	
I.	CHANGE	HISTORY			PAGE	1	
II.	INTRODU	CTION		PAGE	3		
III.	TERMINO		PAGE	3			
IV.	ABBREVI	ATIONS		PAGE	6		
V.	NOTES			PAGE	7		
VI.	INTERFA	CE CONNECTO		PAGE	13		
VII.	INTERFA	CE SCHEMATI	C		PAGE	15	
VIII.	INTERFA	CE DEFINITI	ON				
		KSC					
CONN	MSFC	CABLE	CONNECTS	LOCAT	CION		
NO.	CONN		WITH				
1	13/2AIJ13	NS-GI4 J3	MILA	LCC		PAGE	16
2	1254A1.T14	NS-G14J4	MTLA	LCC		PAGE	19

MSFC/KSC INTERFACE CONTROL DOCUMENT

DEFINITION OF ELECTRICAL INTERFACE

SATURN IB 100 DAY TYPE I

COUNT CLOCK SYSTEM TO

KSC TIMING

I. CHANGE HISTORY

AS REQUIREMENTS CHANGE, APPROVED CHANGES WILL BE ISSUED AGAINST THIS DOCUMENT. REQUESTS FOR CHANGES OR QUESTIONS PERTAINING TO THIS DOCUMENT SHOULD BE DIRECTED TO THE LAUNCH OPERATIONS ELECTRICAL SUB-PANEL.

THIS ICD ALSO INCLUDES A HISTORICAL RECORD OF ICD REVISION/IRN BASELINE AT TIME OF LAUNCH. SEE PAGE 2.

		HISTORIC	AL RECO	RD OF I	CD REV	ISION/IRN	BASEL	INE AT	TIME (OF LAU	NCH	Γ		τ
NON-INCORPORATE	O IRN's													
									8	- =				
									100					
REVISION										,				
LAUNCH DATE														
EFFECTIVITY:	sc													
	LV	206								1 * _				

II. INTRODUCTION

THE PURPOSE OF THIS DOCUMENT IS TO ESTABLISH AND DEFINE THE CONNECTOR

TYPES, CONNECTOR NUMBERS, PIN FUNCTIONS AND OTHER REQUIREMENTS PERTAINING

TO THE SATURN IB COUNT CLOCK SYSTEM TO KSC TIMING ELECTRICAL INTERFACE.

SPECIAL REQUIREMENTS AND RESTRICTIONS DEFINED IN THIS DOCUMENT SHALL BE

IMPOSED BY BOTH KENNEDY SPACE CENTER AND MARSHALL SPACE FLIGHT CENTER TO

ENSURE CONPATIBLE ELECTRICAL MATING AND PROPER SYSTEM PERFORMANCE.

III. TERMINOLOGY

AN EXPLANATION OF THE TERMINOLOGY USED IN THE BODY OF THIS DOCUMENT IS PRESENTED IN THE FOLLOWING PARAGRAPHS:

PN - PIN DESIGNATION

THIS IDENTIFIES THE PIN TO WHICH THE INTERFACE SIGNAL IS ASSIGNED AND DEFINES BOTH ENDS OF THE CABLE ASSOCIATED WITH THE INTERFACE CONNECTOR.

III. TERMINOLOGY - CONTINUED

F - FUNCTION

CLASSIFIES THE TYPE OF SIGNAL AS FOLLOWS:

C - COMMAND

H - SHIELD

M - MEASUREMENT

S - SUPPLY

TITLE

TITLE BRIEFLY STATES THE PURPOSE AND NEED FOR THE SIGNAL.

LOGIC 1 - LOGIC ONE

DEFINES THE CONDITION AND SPECIFIES THE MAXIMUM ALLOWABLE TOLERANCE OF A LOGIC ONE OUTPUT.

LOGIC 0 - LOGIC ZERO

DEFINES THE CONDITION AND SPECIFIES THE MAXIMUM ALLOWABLE TOLERANCE OF A LOGIC ZERO OUTPUT.

III. TERMINOLOGY - CONTINUED

MAX DRIVE - MAXIMUM DRIVE

THIS SPECIFIES EITHER THE LOAD CURRENT OR RESISTANCE VALUE.

REMARKS

PROVIDES ADDITIONAL INFORMATION OR REFERS TO NOTE NUMBERS
THAT ARE LISTED IN PARAGRAPH V OF THIS DOCUMENT.

EFFEC - EFFECTIVITY

BLANK	VEHICLE EFFECTIVITY OF DOCUMENT
8	VEHICLE EFFECTIVITY AGAINST AS-208 ONLY
6, 8, 10	VEHICLE EFFECTIVITY AGAINST AS-206, AS-208
	AND AS-210 ONLY
8 - 10	VEHICLE EFFECTIVITY AGAINST AS-208, AS-209
	AND AS-210 ONLY
8 SUBS	VEHICLE EFFECTIVITY AGAINST AS-208 AND
	SUBSEQUENT

IRN - INTERFACE REVISION NOTICE

REFERS TO THE IRN NUMBER BY WHICH THE SIGNAL WAS CHANGED, ADDED OR DELETED.

IV. ABBREVIATIONS

THE FOLLOWING IS A LIST OF ABBREVIATIONS USED IN THIS DOCUMENT WHICH ARE NOT LISTED IN OR IN AGREEMENT WITH MSFC-STD-350A OR MIL-STD-12C:

CCS - Count Clock System

DUR - Duration

IRIG - Inter Range Instrumentation Group

MA - Milliamps

MILA - Merritt Island Launch Area

MS - Milliseconds

PPS - Pulse Per Second

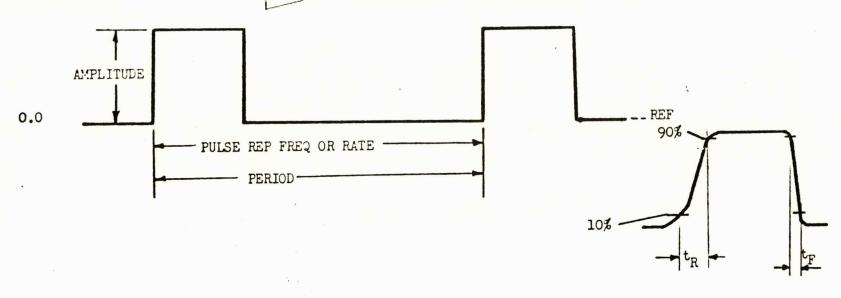
TBD - To Be Determined

V - Volt

V. NOTES

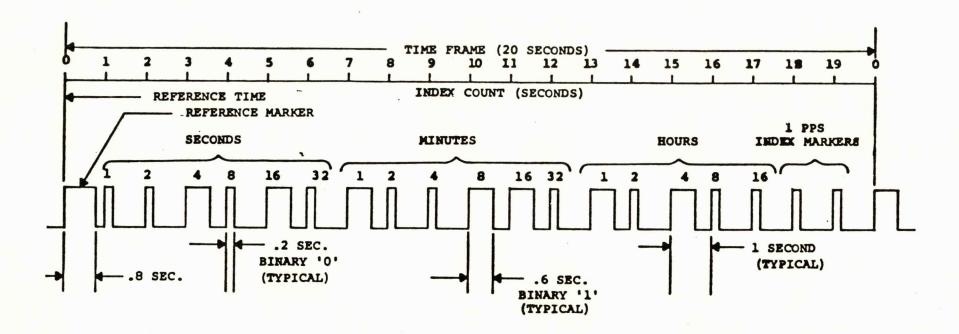
- 1. POSITIVE GOING EDGE IS COHERENT WITH TIME DATA.
- 2. SEE PAGES 8, 9, 10, 11 AND 12 FOR TIMING SIGNAL CHARACTERISTICS.
- 3. AUTOMATIC IS DEFINED AS A LOGIC ONE.
- 4. CONTROLLED 1PPS DOES NOT EXIST DURING HOLD. DUTY
 CYCLE IS 0.5 PER CENT.





RISE	TIME	&	FALL	TIME	
)			

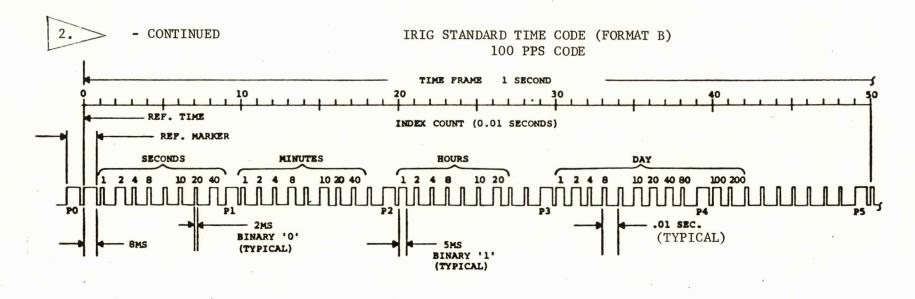
PULSE REP FREQ OR RATE	1 PPS	100 PPS	2 000 PPS
WAVEFORM	SQUARE WAVE	SQUARE WAVE	SQUARE WAVE
AMPLITUDE	0 → -8V <u>+</u> 1V	0 → -3 V [±] 1V	0 → -8V ±1V
DUTY CYCLE	50%	50%	40%
RISE TIME tR	≤ 5 <i>P</i> S	≤ 54 s	≤ 54s
FALL TIME tF	≤ 5 / S	≤ 54s	≤ 5 µs
PERIOD 1/PRF	1 SEC	10 ms	0.5 ms

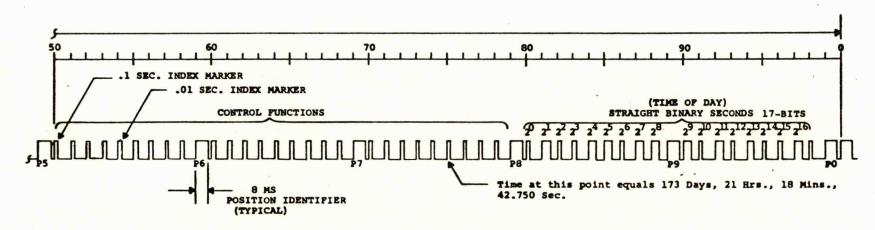


The AMR B-1 Time Code is a 17-digit width modulated code. The code is composed of a Reference Marker, three sub-code words of seconds, minutes and hours and index markers. Each sub-code word is weighted in binary fashion.

The leading edges of all pulses are precisely spaced at 1-second intervals

The code Time Frame is 20 seconds. The sum of the coded time refers to the leading edge of the code word Reference Marker.





IRIG FORMAT B - CONT'D

TIME: UNIVERSAL TIME (UTC)

TIME FRAME: 1.0 SECOND

BINARY CODED DECIMAL TIME-OF-YEAR CODE WORD-30 BINARY DIGITS

- (1) SECONDS, MINUTES, HOURS AND DAYS
- (2) RECYCLES YEARLY

STRAIGHT BINARY TIME-OF-DAY CODE WORD-17 BINARY DIGITS

- (1) SECONDS ONLY
- (2) RECYCLES EACH 24 HOURS

CODE WORD STRUCTURE:

BCD: WORD BEGINS AT INDEX COUNT 1. BINARY CODED ELEMENTS OCCUR BETWEEN POSITION IDENTIFIER ELEMENTS (7 FOR SECONDS: 7 FOR MINUTES; 6 FOR HOURS; 8 AND 2 FOR DAYS) UNTIL THE CODE WORD IS COMPLETE. ONE CR MORE INDEX MARKERS OCCUR BETWEEN DECIMAL DIGITS IN EACH GROUP TO PROVIDE SEPARATION FOR VISUAL RESOLUTION.

SB: WORD BEGINS AT INDEX COUNT 80. FIVE DECIMAL DIGITS (17 BINARY CODED ELEMENTS) OCCUR WITH A POSITION IDENTIFIER BETWEEN THE 9TH AND 10TH BINARY CODED ELEMENTS.

LEAST SIGNIFICANT DIGIT OCCURS FIRST.

ELEMENT RATES AVAILABLE:

- a. 100 PER SECOND (BASIC ELEMENT RATE)
- b. 10 PER SECOND
- c. 1 PER SECOND

2. - CONTINUED

ELEMENT IDENTIFICATION.

- a. "ON TIME" REFERENCE POINT FOR ALL ELEMENTS IS THE LEADING EDGE.

- d. POSITION IDENTIFIER 10 PER SECOND....................... MILLISECONDS. (REFERS TO THE LEADING EDGE OF THE SUCCEEDING ELEMENT)
- e. REFERENCE MARKER 1 PER SECOND......TWO CONSECUTIVE POSITION IDENTIFIERS.

(THE "ON TIME" POINT, TO WHICH THE CODE WORD REFERS, IS THE LEADING EDGE OF THE SECOND POSITION IDENTIFIER.)

RESOLUTION: 10 MILLISECONDS (UNMODULATED)

REP RATE: 100 PPS

VOLTAGE LEVEL: LOGIC 0 -8+1V, LOGIC 1 0+1V

POLARITY: NEGATIVE

VI. INTERFACE CONNECTOR TABLE

THE SATURN IB COUNT CLOCK SYSTEM TO KSC TIMING ELECTRICAL INTERFACE

SHALL CONSIST OF TWO CONNECTORS THAT MATE TO THE MERRITT ISLAND LAUNCH

AREA FROM THE COUNT CLOCK SYSTEM.

INTERFACE CONN NO 1

MSFC CONN 1372A1J13

CONN TYPE MS3124E-24-61P

CABLE CONN TBD

CONN TYPE MS3126E-24-61S

THIS CONNECTOR SHALL PROVIDE COUNT TIME INCREMENTS OF SECONDS, MINUTES HOURS, AND DAYS TO THE MERRITT ISLAND LAUNCH AREA AT THE LCC.

VI. INTERFACE CONNECTOR TABLE - CONTINUED

INTERFACE CONN NO 2

MSFC CONN 1254A1J14

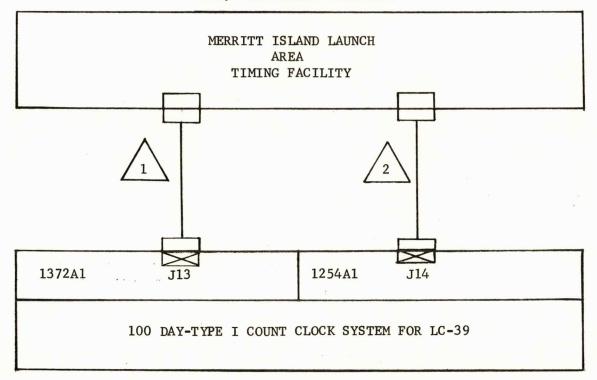
CONN TYPE MS3124E-24-61P

CABLE CONN

CONN TYPE MS3126E-24-61S

THIS CONNECTOR SHALL PROVIDE IRIG TIME CODE AND TIMING PULSES TO THE CCS FROM THE MERRITT ISLAND LAUNCH AREA AT THE LCC.

VII. INTERFACE SCHEMATIC



NOTES:

- 1. SYMBOL DENOTES INTERFACE CONNECTOR NUMBER.
- 2. SYMBOL DENOTES MSFC INTERFACE CONNECTOR.

	PN	F	TITLE 4	LOGIC 1	LOGIC 0	MAX DRIVER E M A R K SEFF IRN
	A	М	SECONDS X 1	0+-1 V	-8+-1 V	10 MA
	В	м	SECONDS X 2	0+-1 V	-8+-1 V	10 MA
	С	M	SECONDS X 4	0+-1 V	-8+-1 V	10 MA
111	D	М	SECONDS X 8	0+-1 V	-8+-1 V	10 MA
	E	М	SECONDS X 10	0+-1 V	-8+-1 V	10 MA
	F	М	SECONDS X 20	0+-1 V	-8+-1 V	10 MA
	G	M	SECONDS X 40	0+-1 V	-8+-1 V	10 MA
	Н	M	MINUTES X 1	0+-1 V	-8+-1 V	10 MA
	J	М	MINUTES X 2	0+-1 V	-8+-1 V	10 MA
11	K	M	MINUTES X 4	0+-1 V	-8+-1 V	10 MA
	L	M	MINUTES X 8	0+-1 V	-8+-1 V	10 MA
	M	M	MINUTES X 10	0+-1 V	-8+-1 V	10 MA
	N	M	MINUTES X 20	0+-1 V	-8+-1 V	10 MA
	P	M	MINUTES X 40	0+-1 V	-8+-1 V	10 MA
•	R	M	HOURS X 1	0+-1 V	-8+-1 V	10 MA
	S	M	HOURS X 2	0+-1 V	-8+-1 V	10 MA
	Τ.	M	HOURS X 4	0+-1 V	-8+-1 V	10 MA
	U	M	HOURS X 8	0+-1 V	-8+-1 V	10 MÅ
	٧	M	HOURS X 10	0+-1 V	-8+-1 V	10 MA
	M	M	HOURS X 20	0+-1 V	-8+-1 V	10 MA
A 1	X	M	DAYS X1	0+-1 V	-8+-1 V	10 MA
es a	Y	M	DAYS X2	0+-1 V	-8+-1 V	10 MA
	Z	M	DAYS X4	0+-1 V	-8+-1 V	10 MA
	A +	M	DAYS X8	0+-1 V	-8+-1 V	10 MA
	8+	M	DAYS X10	0+-1 V	-8+-1 V	10 MA
	C+	М	DAYS X20	0+-1 V	-8+-1 V	10 MA
1	D+	M	DAYS X40	0+-1 V	-8+-1 V	10 MA

CONNECTOR NO 001 MSFC 1372A1J13 65[CD9304 PAGE 16

	PN	F	TITLE	LOGIC 1	LOGIC 0	MAX DRIVE	REMARKSEFF	IRN
	E+	м	DAYS X80	0+-1 V	-8+-1 V	10 MA		
	F+	M	CARRY NOT IN PROGRESS	0+-1 V	-8+-1 V	10 MA	•	
	G+	м	AUTO/MANUAL	0+-1 V	-8+-1 V	10 MA	SEE NOTE 3	
i	H+	Ħ	COUNTING DOWN INDICATION	0+-1 V	-8+-1 V	10 MA	**************************************	
	1+	M	COUNTING UP INDICATION	0+-1 V	-8+-1 V	10 MA		
	J÷	M	HOLDING INFORMATION	0+-1 V	-8+-1 V	10 MA		
	K+	S	1 PPS UNCONTROLLED	0+-1 V	-8+-1 V	10 MA	SEE NOTE 2	
	M+	S	SIGNAL GROUND					
1	N+	S	SIGNAL GROUND					
	P+	S	1 PPS CONTROLLED 5 MS DUR	0+-1 V	-8+-1 V	10 MA	SEE NOTE 4	
	9+		SPARE					
	R+	C	TIMED OUTPUT CMD	28 VDC	o VDC	10 MA		
	S+		SPARE					
	T+		SPARE			2 5		
2	U+		SPARE					
1	٧+		SPARE					
	W+		SPARE					
	X+		SPARE					
	Y+		SPARE					
	Z+		SPARE					
	AA		SPARE					
	88		SPARE					
	CC		SPARE					
	DD		SPARE					
	EE		SPARE					
	FF		SPARE					
,	GG		SPARE					

CONNECTOR NO 001 MSFC 1372A1J13

```
PN F ------ TITLE =----- LOGIC 1 LOGIC 0 MAX DRIVE -----R E M A R K S ------ IRN

HH SPARE

JJ SPARE

KK SPARE

LL SPARE

MM SPARE
```

PP H SHIELD CONNECTED AT CCS ONLY

SPARE

1

651CD9304 PAGE 18

	PN	F	TITLE	LOGI	C 1	LOGIC	0	MA)	DRIVE		R	EMARKS	 EFF
(A	М	B1 CODED TIMING SIGNAL	0+-1	V	-8+-1	V	1 K	ОНН	SEE	NOTES	182	
	8	S	SIGNAL RETURN										
	C	М	100 PPS SQUARE WAVE	0+-1	٧	-8+-1	٧	1 K	ОНМ	SEE	NOTES	182	
	D	S	SIGNAL RETURN										
	E	M	2000 PPS SQUARE WAVE	0+-1	V	-8+-1	٧	1 K	OHM	SEE	NOTES	182	
	F	S	SIGNAL RETURN										
(G	M	IRIG FORMAT B	0+-1	٧	-8+-1	٧	1 K	ОНМ	SEE	NOTES	182	
	Н	S	SIGNAL RETURN										
į	J		SPARE										
	K		SPARE										
	L		SPARE										
	M		SPARE										
	N		SPARE										
	P		SPARE										
1	R		SPARE										
	S		SPARE										
	T		SPARE										
	Ü		SPARE										
	V		SPARE										
	W		SPARE										
1	X		SPARE										
	Y		SPARE										
	Z		SPARE										
	A +		SPARE										
	8+	x * * *	SPARE		* *								
	C+		SPARE										
1	D+		SPARE										

IRN

CONNECTOR NO 002 MSFC 1254A1J14 651CD9304 PAGE 19

```
PN F ----- TITLE ----- LOGIC 1 LOGIC 0 MAX DRIVE ----- E M A R K S ----- IRN
  E+
        SPARE
  F+
        SPARE
, G+
        SPARE
        SPARE
  H+
        SPARE
  1+
        SPARE
  K.
        SPARE
  M+
        SPARE
        SPARE
  N+
        SPARE
        SPARE
  0+
        SPARE
  R+
        SPARE
  S.
  T+
        SPARE
  U+
        SPARE
        SPARE
  V+
  W+
        SPARE
        SPARE
  X+
        SPARE
  Y+
        SPARE
  Z+
1 AA
        SPARE
        SPARE
  BB
  CC
        SPARE
        SPARE
  DD
  EE
        SPARE
  FF
        SPARE
  GG
        SPARE
```

PP H SHIELD GROUNDED AT CCS ONLY

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CONNECTOR NO 002 MSFC 1254A1J14 651CD9304 PAGE 21