J1 712527-14-5P

4

5

A	28 VDC Neg. Return
B	+28 VDC
C	28 VDC Neg. Return
D	+28 VDC
E	No Connection

J2 712527-14-18P

from 301

A	Data Input 2^9
B	Data Input 2^8
C	Data Input 2^7
D	Data Input 2^6
E	Data Input 2^5
F	Data Input 2^4
G	Data Input 2^3
H	Data Input 2^2
J	Data Input 2^1
K	Data Input 2^0
L	Data Input Common
M	No Connection
N	No Connection
R	No Connection
S	No Connection
T	No Connection
U	Assembly Chassis Grd.
P	No Connection

0 ~ 5V

12

18

J3 712527-12-10P

Sync

A	Sync Common
B	3600 pps Sync.
C	3600 pps Sync.
D	Sync Common

J3 712527-12-10P (cont.)

E	No Connection
F	4 pps
G	No Connection
H	No Connection
J	4 pps
K	Assembly Chassis Grd.

J4 712527-14-18S

LVDA

A	Data Output 2^9
B	Data Output 2^8
C	Data Output 2^7
D	Data Output 2^6
E	Data Output 2^5
F	Data Output 2^4
G	Data Output 2^3
H	Data Output 2^2
J	Data Output 2^1
K	Data Output 2^0
L	Data Output Common
M	Data Ready Signal
N	Data Output Common
R	No Connection
S	No Connection
T	No Connection
U	Assembly Chassis Grd.
P	No Connection

14

18

A

J5 712527-16-26P

1	A	Address bit #1 (3600 pps)	GP A = 28V, GP B = 7
2	B	Address bit #2 (30 Counter stage 1)	1
3	C	Address bit #3 (30 Counter stage 2)	2
4	D	Address bit #4 (30 Counter stage 3)	4
5	E	Address bit #5 (30 Counter stage 4)	8
6	F	Address bit #6 (30 Counter stage 5)	16
7	G	Address bit #7 (10 Counter stage 1)	1
8	H	Address bit #8 (10 Counter stage 2)	2
9	J	Address bit #9 (10 Counter stage 3)	4
10	K	Address bit #10 (10 Counter stage 4)	8
11	L	Address bit #11 (3 Counter stage 1)	1
12	M	Address bit #12 (3 Counter stage 2)	2
13	N	Address Input Common	
14	P	Address Input Common	
15	R	Data Request Signal	28V
	S	No Connection	
	T	No Connection	
16	U	Assembly Chassis Grd.	

CH

FR

MUX

from LVDA

28V on

7 off

28V

7 off

16

18

Walt

Dick
539-5205

SLOT	CARD NAME	DRAWING NUMBER SCHEMATIC	PWA
A3	Clock	50M67507	50M67510
A4	Sync Detector and Master Frame Reset	50M67511	50M67514
A5	Thirty Counter	50M67515	50M67518
A6	Three and Ten Counter	50M67519	50M67522
A7	Group and Channel Comparator	50M67523	50M67526
A8	Mux Comparator	50M67527	50M67530
A9	Frame Comparator	50M67531	50M67534
A20	Data Entry Control	50M67539	50M67542
A22	Data Entry Gate	50M67535	50M67538
A19	Register	50M67543	50M67546
A18	Output Driver	50M67547	50M67550
A2, 10, 11, 12, 13, 14, 15, 16, 17, 21	Dummy	None	50M66216
A1	DC-DC Converter	50M66612	50M68524
A24	5 Volt Regulator	50M67467	50M67470
None	Mother Board	50M67542	50M67486

A-1

DC-DC Converter

Pin	Function
A	+6 Volt Output
B	+12 Volt Output
C	-6 Volt Output
E	Output Common
W	Input Common
X	Input Common
a	28 Volt Input
b	28 Volt Input
D	Spare
F	Spare
G	Spare
H	Spare
I	Spare
J	Spare
K	Spare
L	Spare
M	Spare
N	Spare
O	Spare
P	Spare
Q	Spare
R	Spare
S	Spare
T	Spare
U	Spare
V	Spare
Y	Spare
Z	Spare

A-24

5 Volt Regulator

Pin	Function
U	Grd
S	+5V
Q	AC in.
O	AC in.
A	Spare
B	Spare
C	Spare
D	Spare
E	Spare
F	Spare
G	Spare
H	Spare
I	Spare
J	Spare
K	Spare
L	Spare
M	Spare
N	Spare
P	Spare
R	Spare
T	Spare
V	Spare
W	Spare
X	Spare
Y	Spare
Z	Spare
a	Spare
b	Spare

CIU PIN FUNCTION LIST FOR PC CARDS

A-3 Clock		A-4 Master Frame Reset	
Pin	Function	Pin	Function
T	Sync (3600 cps)	T	4 pps Sync
S	+5V	a	+6V
U	Internal Grd. for 5V	Y	-6V
a	+6V	W	Logic Grd.
Y	-6V	S	5 volts
W	Logic Ground	U	5V Grd.
Q	30	P	R1
B	Data Entry Clock	R	R2
A	Spare	N	R3
C	Spare	M	$\overline{R1}$
D	Spare	K	$\overline{R2}$
E	Spare	I	$\overline{R3}$
F	Spare	B	Sync Indication
G	Spare	A	Spare
H	Spare	C	Spare
I	Spare	D	Spare
J	Spare	E	Spare
K	Spare	F	Spare
L	Spare	G	Spare
M	Spare	H	Spare
N	Spare	J	Spare
O	Spare	L	Spare
P	Spare	O	Spare
R	Spare	Q	Spare
V	Spare	V	Spare
X	Spare	X	Spare
Z	Spare	Z	Spare
b	Spare	b	Spare

A-5

30 Counter

Pin	Function
a	+6 V
Y	-6 V
W	Grd.
Q	Clock
M	$\overline{R1}$
B	Channel 0
D	Channel 1
F	Channel 2
H	Channel 3
J	Channel 4
T	R1
A	Spare
C	Spare
E	Spare
G	Spare
I	Spare
K	Spare
L	Channel 3
N	Spare
O	Spare
P	R1
R	Spare
S	Spare
U	Spare
V	Spare
X	Spare
Z	Spare
b	Spare

A-6

3 & 10 Counter

Pin	Function
a	+6V
Y	-6V
W	Grd.
M	$\overline{R1}$
K	$\overline{R2}$
I	$\overline{R3}$
A	FR0
C	FR1
E	FR2
G	FR3
R	R2
O	M0
N	M1
P	R3
B	Spare
D	Spare
F	Spare
H	Spare
J	Spare
L	Spare
Q	Spare
S	Spare
T	Spare
U	Spare
V	Spare
X	Spare
Z	Spare
b	Spare

A-7

Group and Channel Comparator

Pin	Function
T	GP.
N	GA.
C	CH0
M	CA0
E	CH1
L	CA1
G	CH2
R	CA2
I	CH3
O	CA3
J	CH4
P	CA4
A	Gp. and Chan. Compare
a	+6V
Y	-6V
W	Logic Grd.
B	Spare
D	Spare
F	Spare
H	Spare
K	Spare
Q	Spare
S	Spare
U	Spare
V	Spare
X	GP. and Chan. Compare
Z	Spare
b	Spare

A-20

Date Entry Control

Pin	Function
D	Clock
B	Sync
A	30 Compare
G	10 Compare
E	3 Compare
C	Data Request
a	+6V
Y	-6V
W	Grd.
F	+5V
S	Int. Grd.
H	Spare
I	Spare
J	Spare
K	Spare
L	Spare
M	Spare
N	Data Ready
O	Spare
P	Spare
Q	Spare
R	Spare
T	Data Strobe
U	Reset Register
V	Reset Register
X	Reset Register
Z	Spare
b	Spare

A-9

Frame Comparator

Pin	Function
B	FR0
J	FA0
D	FR1
L	FA1
F	FR2
N	FA2
H	FR3
P	FA3
a	+6V
Y	-6V
W	Logic Grd.
A	Frame Compare
C	Spare
E	Spare
G	Spare
I	Jumper
K	Spare
M	Spare
O	Spare
Q	Spare
R	Spare
S	Spare
T	Spare
U	Spare
V	Jumper
X	Frame Compare
Z	Spare
b	Spare

A-8

Mux Comparator

Pin	Function
O	M0
L	MA0
T	M1
N	MA1
a	+6V
Y	-6V
W	Logic Grd.
R	Data Request
A	$\overline{\text{Data Request}}$
B	Mux Compare
C	Spare
D	Spare
E	Spare
F	Spare
G	Spare
H	Spare
I	Spare
J	Spare
K	Spare
M	Spare
P	Spare
Q	Spare
S	Spare
U	Spare
V	Spare
X	Mux Compare
Z	Spare
b	Spare

A-18

Output Driver

Pin	Function
W	Grd.
Y	-6V
Q	$2^{8}0$
S	$2^{9}0$
T	$\overline{2^{9}0}$
R	$\overline{2^{8}0}$
P	$\overline{2^{7}0}$
N	$\overline{2^{6}0}$
M	$2^{6}0$
O	$2^{7}0$
I	$2^{4}0$
K	$2^{5}0$
L	$\overline{2^{5}0}$
J	$\overline{2^{4}0}$

A-18 (cont.)

Pin	Function
H	$\overline{2^{3}0}$
F	$\overline{2^{2}0}$
E	$2^{2}0$
G	$2^{3}0$
A	$2^{0}0$
C	$2^{1}0$
D	$\overline{2^{1}0}$
B	$\overline{2^{0}0}$
X	$\overline{\text{Data Ready}}$
U	Data Ready
V	Spare
Z	Spare
a	Spare
b	Spare

A-19

Register

Pin	Function
a	+6V
Y	-6V
W	Logic Grd.
S	$\overline{2^{9}0}$
V	$2^{9}1$
X	Reset
Q	$\overline{2^{8}0}$
T	$2^{8}1$
O	$\overline{2^{7}0}$
R	$2^{7}1$
M	$\overline{2^{6}0}$
P	$2^{6}1$
K	$\overline{2^{5}0}$
N	$2^{5}1$

A-19 (cont.)

Pin	Function
I	$\overline{2^{4}0}$
L	$2^{4}1$
U	Reset
G	$\overline{2^{3}0}$
J	$2^{3}1$
E	$\overline{2^{2}0}$
H	$2^{2}1$
C	$\overline{2^{1}0}$
F	$2^{1}1$
A	$\overline{2^{0}0}$
D	$2^{0}1$
B	Spare
Z	Spare
b	Spare

A22

Data Entry Gate

Pin	Function
a	+6V
V	+5V
X	Grd.
W	Grd.
S	2^{9^1}
T	2^9
Q	2^{8^1}
U	Control
R	2^8
O	2^{7^1}
P	2^7
M	2^{6^1}
N	2^6
K	2^{5^1}
L	2^5

A22 (cont.)

Pin	Function
I	2^{4^1}
J	2^4
G	2^{3^1}
H	2^3
E	2^{2^1}
F	2^2
C	2^{1^1}
D	2^1
A	2^{0^1}
B	2^0
Y	Spare
Z	Spare
b	Spare