NOTES:
1. GENERAL REQUIREMENTS FOR EACH SECTION, UNLESS OTHERWISE SPECIFIED:
   A. UNIT SHALL BE IN ACCORDANCE WITH MIL-S-19500, IN ADDITION TO THE REQUIREMENTS SPECIFIED HEREIN.
   B. ABSOLUTE MAXIMUM RATINGS AT 25 ± 3 DEGREES CENTIGRADE, AMBIENT:
      - COLLECTOR TO EMITTER VOLTAGE (VCEO): 60 VOLTS DC
      - COLLECTOR TO EMITTER VOLTAGE (VCEO): 80 VOLTS DC (RBE < 10f)
      - COLLECTOR TO BASE VOLTAGE (VCBO): 100 VOLTS DC
      - EMITTER TO BASE VOLTAGE (VEBO): 7 VOLTS DC
      - POWER DISSIPATION: (EACH SECTION)
        - AT 25°C CASE TEMPERATURE: 1.5 WATTS
        - AT 100°C CASE TEMPERATURE: 860 MILLIWATTS
        - AT 25°C AMBIENT TEMPERATURE: 500 MILLIWATTS
      - POWER DISSIPATION (DUAL) TOTAL:
        - AT 25°C CASE TEMPERATURE: 3 WATTS
        - AT 100°C CASE TEMPERATURE: 1.7 WATTS
        - AT 25°C AMBIENT TEMPERATURE: 600 MILLIWATTS
   C. TEMPERATURE RANGE:
      - STORAGE: MINUS 65 TO PLUS 200 DEGREES CENTIGRADE.
      - OPERATING JUNCTION: PLUS 200 DEGREES CENTIGRADE, MAX.
   D. ELECTRICAL CHARACTERISTICS: PER TABLE I AND II.
      - hFE: AS SPECIFIED HEREIN WITH MAXIMUM ACCEPTANCE NUMBER 3 PER MIL-S-19500.
      - TEST DATA SHALL BE FURNISHED UPON REQUEST.
      - OWNERS AND OTHER ACCESS TO MANUFACTURING LOT MAY BE FURNISHED FROM ADDITIONAL LOTS. SUPPLIER SHALL CONFORM WITH THE QUALITY ASSURANCE PROVISIONS OF MIL-1015404 SECTION.
   E. MARKING: PER MIL-S-19500 AND SHALL INCLUDE THE MANUFACTURER’S NAME AND SIGNATURE, DATE CODE, AND TYPE NUMBER. THE NASA NO. SHALL APPEAR ON EACH INTERIOR AND EXTERIOR SHIPPING CONTAINER AS WELL AS ON A TAG TO BE INCLUDED IN EACH SHIPPING CONTAINER.
2. CONSTRUCTION REQUIREMENTS:
   A. SEMICONDUCTOR: SILICON, NPN, DOUBLE-DIFFUSED
   B. CASE: METAL, SUITABLY PROTECTED TO WITHSTAND ENVIRONMENTAL REQUIREMENTS.
   C. LEADS: SIX (6), SOLDERABLE, FLEXIBLE AND WELDABLE SOLID "KOVAR" CONDUCTORS, GREASE AND OIL FREE; GOLD FLASHED PER ND PS1015402. A CERTIFICATE OF COMPLIANCE FOR LEAD MATERIAL SHALL ACCOMPANY EACH LOT SHIPPED.
   D. HEADER: SHALL BE HERMETICALLY (GLASS-KOVAR) SEALED AND IMPERVIOUS TO LIGHT.
   E. QUALITY ASSURANCE:
      - MATERIAL: SHALL MEET THE REQUIREMENTS OF ND 1002000
      - PULSE CONDITIONS: WIDTH = 300µsec; DUTY CYCLE < 1%.
      - ABSENCE OF FE: VALUES SHOWN IN PARENTHESES IN TABLES, ARE APPLICABLE ONLY FOR 1010376-3.
3. ENVIRONMENTAL REQUIREMENTS: SHALL MEET THE REQUIREMENTS OF ND 1002000
4. INTERPRET DRAWING IN ACCORDANCE WITH STANDARDS PRESCRIBED BY MIL-S-70327.

ORIGINAL SOURCE OF SUPPLY:
FAIRCHILD SEMICONDUCTOR CORP.
545 WHISMAN ROAD
MOUNTAIN VIEW, CALIFORNIA
1010376-1 TYPE NO. SP-8126
1010376-2 TYPE NO. SP8127
SAME AS 1010376-1, EXCEPT FOR hFE=50 MIN AT IC=SmA, VCE=1 V AND BVCEO=80 V MIN AT IC=10 mA.

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ON TRIMS DECIMALS ANGLES " NOT SCALE THIS DRAWING

CLASS B RELEASE TDR No. 1042 DATE 12/21/64

FOR INFORMATION ONLY
MANNED SPACECRAFT CENTER HOUSTON, TEXAS
SEMICONDUCTOR DEVICE TRANSISTOR, SILICON, DUAL SECTION
SPECIFICATION CONTROL DRAWING

LIST OF MATERIALS
M.I.T. INSTRUMENTATION LAB
CAMBRIDGE, MASS.

APPROVAL
CHECKED
MIT APPROVAL
SHEET 1 OF 3