#### Performance-based assessments for DC circuit competencies

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The purpose of these assessments is for instructors to accurately measure the learning of their electronics students, in a way that melds theoretical knowledge with hands-on application. In each assessment, students are asked to predict the behavior of a circuit from a schematic diagram and component values, then they build that circuit and measure its real behavior. If the behavior matches the predictions, the student then simulates the circuit on computer and presents the three sets of values to the instructor. If not, then the student then must correct the error(s) and once again compare measurements to predictions. Grades are based on the number of attempts required before all predictions match their respective measurements.

You will notice that no component values are given in this worksheet. The *instructor* chooses component values suitable for the students' parts collections, and ideally chooses different values for each student so that no two students are analyzing and building the exact same circuit. These component values may be hand-written on the assessment sheet, printed on a separate page, or incorporated into the document by editing the graphic image.

This is the procedure I envision for managing such assessments:

- 1. The instructor hands out individualized assessment sheets to each student.
- 2. Each student predicts their circuit's behavior at their desks using pencil, paper, and calculator (if appropriate).
- 3. Each student builds their circuit at their desk, under such conditions that it is impossible for them to verify their predictions using test equipment. Usually this will mean the use of a multimeter only (for measuring component values), but in some cases even the use of a multimeter would not be appropriate.
- 4. When ready, each student brings their predictions and completed circuit up to the instructor's desk, where any necessary test equipment is already set up to operate and test the circuit. There, the student sets up their circuit and takes measurements to compare with predictions.
- 5. If any measurement fails to match its corresponding prediction, the student goes back to their own desk with their circuit and their predictions in hand. There, the student tries to figure out where the error is and how to correct it.
- 6. Students repeat these steps as many times as necessary to achieve correlation between all predictions and measurements. The instructor's task is to count the number of attempts necessary to achieve this, which will become the basis for a percentage grade.
- 7. (OPTIONAL) As a final verification, each student simulates the same circuit on computer, using circuit simulation software (Spice, Multisim, etc.) and presenting the results to the instructor as a final pass/fail check.

These assessments more closely mimic real-world work conditions than traditional written exams:

- Students cannot pass such assessments only knowing circuit theory or only having hands-on construction and testing skills they must be proficient at both.
- Students do not receive the "authoritative answers" from the instructor. Rather, they learn to validate their answers through real circuit measurements.
- Just as on the job, the work isn't complete until all errors are corrected.
- Students must recognize and correct their own errors, rather than having someone else do it for them.
- Students must be fully prepared on exam days, bringing not only their calculator and notes, but also their tools, breadboard, and circuit components.

Instructors may elect to reveal the assessments before test day, and even use them as preparatory labwork and/or discussion questions. Remember that there is absolutely nothing wrong with "teaching to

the test" so long as the test is valid. Normally, it is bad to reveal test material in detail prior to test day, lest students merely memorize responses in advance. With performance-based assessments, however, there is no way to pass without truly understanding the subject(s).

 $\overline{\text{Question 1}}$ 

Competency: Voltage divider circuit	Version:
Schematic	
V <sub>supply</sub> =	$R_1 $ $R_2 $ $V_{out}$ $V$ Voltmeter
Given conditions	
$V_{\text{supply}} = V_{\text{out}} =$	
Parameters	
Predicted Measured	Predicted Measured
I <sub>supply</sub>	$I_{R1}$
V <sub>R1</sub>	$I_{R2}$
$V_{R2}$	
$rac{V_{ m out}}{V_{ m supply}}$ (Ratio)	culated (from measurements)
Fault analysis	open other
Suppose component fails	open other
What will happen in the circuit?	increase", "decrease", or "no change" for each parameter:
V <sub>R1</sub>	$I_{R1}$
$V_{R2}$	$I_{R2}$

<u>file 03176</u>

Competency: Current divider cir	cuit	Ve	rsion:
Schematic			
I <sub>supply</sub>	$R_1 \gtrsim I_{out} $ And	$R_2$	
Given conditions			
$I_{\text{supply}} = I_{\text{ou}}$	<sub>ut</sub> =		
Parameters			
Predicted Measured		Predicted	Measured
V <sub>supply</sub>	$V_{R1}$		
$I_{R1}$	$V_{R2}$		
$I_{R2}$	'		
$\frac{I_{\text{out}}}{I_{\text{supply}}} \; \; (\text{Ratio})  \; $	Calculated	(from measui	rements)
Fault analysis	open	other _	
Suppose component fail	s shorte		
What will happen in the circuit?		crease", or "no change" for	each parameter:
I <sub>R1</sub>	$V_{R1}$		
$I_{R2}$	$V_{R2}$		

 $\underline{\mathrm{file}\ 03177}$ 

Competency: Series	-parallel DC resisto	or circuit Version:
Schematic		
V <sub>supply</sub>	$R_1 $ $R_2 $ $R_2 $	$\geqslant$ $R_3$
Given conditions		
$V_{\text{supply}} = R_1$	$R_2 =$	$R_3 =$
Parameters		
	Measured	Predicted Measured
Isupply	Vieasureu	
-supply		I <sub>R1</sub>
V <sub>R1</sub>		$I_{R2}$
$V_{R2}$		I <sub>R3</sub>
V <sub>R3</sub>		
Fault analysis		
	nt fails	open other
Suppose componen	s	shorted
What will happen in	THE CIRCUIT! Write "incre	ease", "decrease", or "no change" for each parameter
V <sub>R1</sub>		$I_{R1}$
V <sub>R2</sub>		$I_{R2}$
V <sub>R3</sub>		$I_{R3}$

<u>file 01633</u>

Competency: Ser	ies-parallel D	C resistor cir	r <b>cuit</b> Ve	ersion:
Schematic V Given conditions	supply	$R_1$ $R_2$ $R_3$		
V <sub>supply</sub> =	$R_1 =$	$R_2 =$	$R_3 =$	
Parameters				
Predicted	Measured		Predicted	Measured
I <sub>supply</sub> V <sub>R1</sub> V <sub>R2</sub>		$\begin{bmatrix} & & I_{R1} \\ & & I_{R2} \\ \end{bmatrix}$		
V <sub>R3</sub>				
Fault analysis		Open	other .	
Suppose compo	nent f	ails short		_
What will happen in the circuit? Write "increase", or "no change" for each parameter:				
V <sub>R1</sub>		$I_{R1}$		
V <sub>R2</sub>		$I_{R2}$		
V <sub>R3</sub>		$I_{R3}$		

<u>file 01631</u>

Competency: Series-parallel DC resistor circuit Version:				
Schematic			- V	70011.
Conomano	·	$V_{\text{supply}}$		
		- $ $ $ $ $ $ $$	7	
		•		
	J		∫ >_n	
]	$R_1 \leqslant \qquad \leqslant R_2$		$\leq$ R <sub>3</sub>	
Given conditions				
$V_{ m supply} =$	$R_1 = R$		R	
supply —	$\mathbf{K}_1$ – $\mathbf{K}_2$	2 —	143 –	
Parameters				
Predicted	Measured		Predicted	Measured
$I_{supply}$		$I_{R1}$		
		-		
V <sub>R1</sub>		$I_{R2}$		
V <sub>R2</sub>		$I_{R3}$		
		Ю		
$V_{R3}$				
Fault analysis			athar	
Suppose compor	nent fails	open s	other	
What will happen		snorte		
	ni ui <del>o</del> oncuit!	ı	lecrease", or "no change" i	or eacn parameter:
V <sub>R1</sub>		$I_{R1}$		
V <sub>R2</sub>		$I_{R2}$		
V <sub>R3</sub>		$I_{R3}$		

 $\underline{\mathrm{file}\ 01632}$ 

Competency: Series-parallel DC resistor circuit Version:			
Schematic			
$\begin{array}{c c} & & & \\ & & \\ & & & \\ & & & \\ & & & \\ & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ &$			
Given conditions			
$V_{\text{supply}} = R_1 = R_2 = R_3 =$			
Parameters			
Predicted Measured Predicted Measured			
I <sub>supply</sub> I <sub>R1</sub>			
$V_{R1}$ $I_{R2}$			
$V_{R2}$ $I_{R3}$			
$V_{R3}$			
Fault analysis			
open   other			
Suppose component shorted shorted			
What will happen in the circuit? write "increase", "decrease", or "no change" for each parameter:			
$V_{R1}$ $I_{R1}$			
$V_{R2}$ $I_{R2}$			
$V_{R3}$ $I_{R3}$			

<u>file 01630</u>

Competency: Series-parallel DC resistor circuit Version:				
Schematic				
$V_{\text{supply}} = R_1$ $R_1$ $R_2$ $R_3$				
Given conditions				
$V_{ m supply} =$	$R_1 =$	$R_2 =$	$R_3 =$	$R_4 =$
Parameters				
Predicted	Measured		Predicted	Measured
$I_{ ext{supply}}$		$I_{R1}$		
V <sub>R1</sub>		$I_{R2}$		
V <sub>R2</sub>		$I_{R3}$		
$V_{R3}$		$I_{R4}$		
V <sub>R4</sub>				
Fault analysis open other				
Suppose component open other shorted  What will happen in the circuit? write "increase", "decrease", or "no change" for each parameter:				
$oxed{V_{R1}} oxed{V_{R3}} oxed{I_{R1}} oxed{I_{R3}}$				
$V_{R2}$	V <sub>R4</sub>	I <sub>R2</sub>		$I_{R4}$

<u>file 01606</u>

Competency: Series-parallel DC resistor circuit Version:				
Schematic				
$V_{\text{supply}} = R_1$ $R_1$ $R_2$ $R_3$ $R_4$				
Given conditions				
$V_{supply} =$	$R_1 =$	$R_2 =$	$R_3 =$	$R_4 =$
Parameters				
Predicted  I <sub>supply</sub> V <sub>R1</sub> V <sub>R2</sub>	Measured	$egin{array}{cccc} & & & & & & & & & & & & & & & & & $	2	Measured
V <sub>R3</sub>		$I_R$	4	
V <sub>R4</sub>				
Fault analysis			n  other	
Suppose component open other shorted  What will happen in the circuit? write "increase", "decrease", or "no change" for each parameter:				
V <sub>R1</sub>	V <sub>R3</sub>	$I_{R1}$		$I_{R3}$
$V_{R2}$	$V_{R4}$	$I_{R2}$		$I_{R4}$

 $\underline{\mathrm{file}\ 01607}$ 

Competency: Series-parallel DC resistor circuit Version:				
Schematic $R_1$ $V_{supply} = R_2$ $R_3$ $R_4$				
Given conditions				
$V_{ m supply}$ =	$R_1 =$	$R_2 =$	$R_3 =$	$R_4 =$
Parameters				
Predicted	Measured	٦	Predicted	Measured
I <sub>supply</sub>		$I_{R1}$		
V <sub>R1</sub>		$I_{R2}$	2	
V <sub>R2</sub>		$I_{R3}$		
V <sub>R3</sub>		$I_{R4}$		
V <sub>R4</sub>				
Fault analysis			n other	
Suppose component open other shorted  What will happen in the circuit? write "increase", "decrease", or "no change" for each parameter:				
V <sub>R1</sub>	V <sub>R3</sub>	$I_{R1}$		$I_{R3}$
$V_{R2}$	V <sub>R4</sub>	$I_{R2}$		I <sub>R4</sub>

<u>file 01608</u>

Competency: Custom rheostat rang	e Version:
Schematic	
R <sub>1</sub> R <sub>pot</sub>	$R_2$
Given conditions	
R <sub>total</sub> (minimum) =	$R_{total}$ (maximum) =
$R_{pot} =$	
Parameters	
Ideal Attained	
	Resistors $R_1$ and $R_2$ may need to be eries-parallel networks in order to chieve the necessary values.
Measured	
R <sub>total</sub> (minimum)	
R <sub>total</sub> (maximum)	
Fault analysis	
Suppose component fails	open other
M/hat will happen in the circuit?	shorted ite "increase", "decrease", or "no change" for each parameter:
R <sub>total</sub> (minimum)	
R <sub>total</sub> (maximum)	

 $\underline{\mathrm{file}\ 01754}$ 

# Competency: Potentiometer as voltage divider Version: Description You must set the potentiometer to the correct position to achieve $\boldsymbol{V}_{out}$ given $\boldsymbol{V}_{supply}$ before it is connected to V<sub>supply</sub> for testing. Schematic Given conditions $V_{out} =$ $V_{\text{supply}} =$ Parameters Measured $\boldsymbol{V}_{\text{out}}$ $V_{\text{out(actual)}} - V_{\text{out(ideal)}} \times 100\%$ Calculated Error (%)

file 01925

Competency: Kirchhoff's Voltage Law	Version:
Schematic	
$V_{\text{supply}} = R_4$	$R_2$ $C$ $R_3$ $D$
Given conditions	
$V_{\text{supply}} = \text{Any whole-number value even}$ $R_1 = R_2 = R_3 = R_4 =$	aly divisible by 4
Parameters	
Predicted Measured	Predicted Measured
$V_{R1}$	$V_{R3}$
$V_{R2}$	$V_{R4}$
$V_{BD}$	$V_{DA}$
V <sub>BE</sub>	$V_{DC}$
V <sub>AC</sub>	V <sub>CE</sub>
Note: "V <sub>BD</sub> " means voltage me	

<u>file 03294</u>

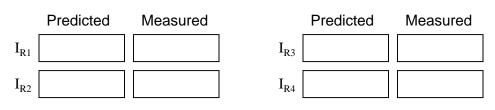
# Competency: Kirchhoff's Current Law Version: Schematic $R_1$ $R_2$ $R_3$ $R_4$ $R_5$ D

# Given conditions

 $I_{\text{supply}} = \mbox{Any}$  whole-number milliamp value evenly divisible by 4

$$R_1 = R_2 = R_3 = R_4 = R_5 =$$

# Parameters



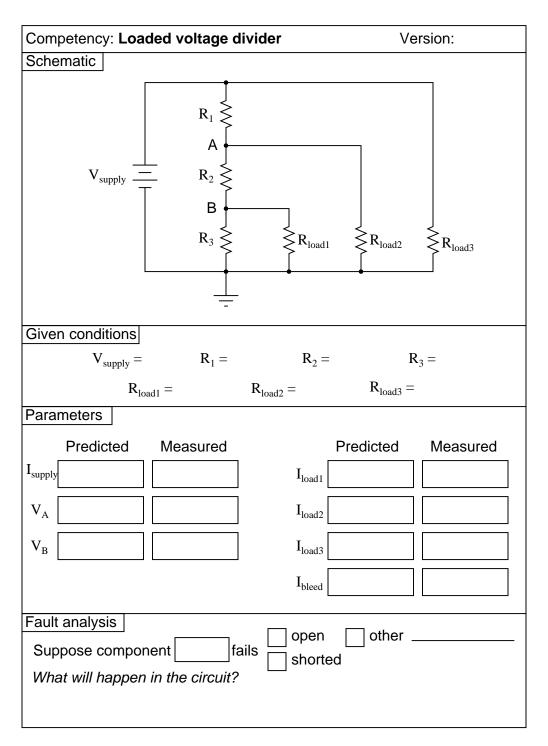
Sketch directions and magnitudes of currents at these nodes:



file 03593

Competency: Loaded voltage divider	Version:
Schematic	
$V_{\text{supply}} = R_2 $ $R_2 $ $R_3 $ $R_3 $	$R_{load1}$
Given conditions	
$V_{\text{supply}} = R_1 = R_2 = R_3 =$	$R_{load1} = R_{load2} =$
Parameters	
Predicted Measured	Predicted Measured
${ m I}_{ m supply}$	d1
$oxed{V_A}$	d2
$oxed{V_{B}}$	ed
Fault analysis	
Suppose component fails sho	en otherorted
M/hat will be a part in the arise vito	", "decrease", or "no change" for each parameter:
V <sub>A</sub> I <sub>load 1</sub>	I <sub>bleed</sub>
V <sub>B</sub> I <sub>load2</sub>	

<u>file 01609</u>



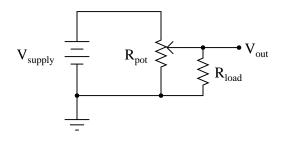
file 01642

# Competency: Potentiometer as loaded voltage divider Version:

### Description

You must set the potentiometer to the correct position to achieve  $V_{out}$  given  $V_{supply}\ \textit{before}$  it is connected to  $V_{supply}$  for testing.

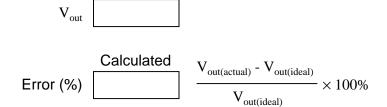
# Schematic



# Given conditions

$$V_{\text{supply}} = V_{\text{out}} = R_{\text{pot}} = R_{\text{load}} =$$

## Parameters



Measured

file 01926

Competency: Wheatstone bridge	Version:			
Schematic				
$V_{\text{supply}} \stackrel{R_1}{=} \qquad \stackrel{R_2}{=} \qquad $	$R_{3}$ $R_{pot}$			
Given conditions				
$V_{\text{supply}} = R_1 = R_2$	$=$ $R_3 =$			
Parameters				
Predicted Measured  R <sub>pot</sub> (balance)				
Fault analysis	pen other			
Suppose component open other shorted				
What will happen in the circuit? Write "increase", "decrease", or "no change" for each parameter:				
V <sub>R1</sub> I <sub>supply</sub>				
V <sub>R2</sub>				
V <sub>R3</sub>				
$V_{R4}$				

 $\underline{\mathrm{file}\ 01618}$ 

Competency: Bridge resistance measurement	Version:
Schematic	
$V_{\text{supply}} = \begin{bmatrix} R_1 & R_2 & R_3 & R_4 & R_5 $	unknown value)
Given conditions	
$V_{\text{supply}} = R_1 = R_2$	=
$R_{pot}$ = Decade resistance box	
Parameters	
Measured Measured by bridge by ohmmeter	
Calculations	

<u>file 01643</u>

Competency: DC voltmeter circuit	Version:
Schematic	
Meter movement	
Test lead Tes	t lead
Given conditions	
$I_{F.S.} = R_{movement} =$	Full-scale range =
Parameters	
Predicte  R <sub>range</sub> Predicte  Meter indication with	
full-scale voltage applied	
Calculations	

<u>file 01649</u>

Competency: Voltmeter loading	Version:			
Schematic				
$V_{\text{supply}} = R_1 $ $R_2 $	† V			
Given conditions				
$V_{supply} = R_1 =$	$R_2 =$			
Explanation				
Due to the effects of the voltmeter "loading" the voltage divider circuit, there will be a significant difference between $V_{\rm R2}$ predicted and $V_{\rm R2}$ measured.				
Parameters				
$V_{R2} \begin{tabular}{ c c c c c } \hline Predicted \\ V_{R2} \begin{tabular}{ c c c c c c c c c c c c c c c c c c c$	no meter connected)			
	urement with voltmeter)			
Calculated Advertised  R <sub>input</sub> (Meter)				

file 01694

Competency: Self-induction	Version:
Schematic	
Pushbutton switch	
$V_{\text{supply}} = $	Neon lamp
Given conditions	
$V_{ m supply} =$	
Parameters	
Yes/no answers only	
Lamp across L <sub>1</sub> Predicted Tester Lamp flashes?	ed
Lamp across switch Lamp flashes?	
Lamp across battery Lamp flashes?	

<u>file 01646</u>

Schematic
$ \frac{L_1}{M}$ $\frac{L_2}{M}$ $\frac{L_3}{M}$ $\frac{L_3}$
Civon conditions
Given conditions
$L_1 = L_2 = L_3 =$
Parameters
Predicted Measured
L <sub>total</sub>
i total

<u>file 01650</u>

Competency: Parallel inductances	Version:			
Schematic				
$\begin{array}{c} L_1 \\ \\ L_2 \\ \\ L_3 \\ \\ \end{array}$				
Given conditions				
$L_1 = L_2 = L_3 =$				
Parameters				
Predicted Measured  L <sub>total</sub>				

<u>file 01651</u>

Competency: Series coupled inductors	Version:
Schematic	
$L_{total}$ or	$L_{total}$
Given conditions	
$L_1 = L_2 =$	
Parameters	
Predicted Measured  L <sub>total</sub>	
Analysis Equation used to calcula	ate L <sub>total</sub> :

<u>file 01989</u>

Competency: Series capacitances	Version:
Schematic	
$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	
Given conditions	
$C_1 = C_2 = C_3 =$	
Parameters	
Predicted Measured	
C <sub>total</sub>	
Ctotal	
Analysis Faustien used to calculate C	
Equation used to calculate $C_{total}$ :	

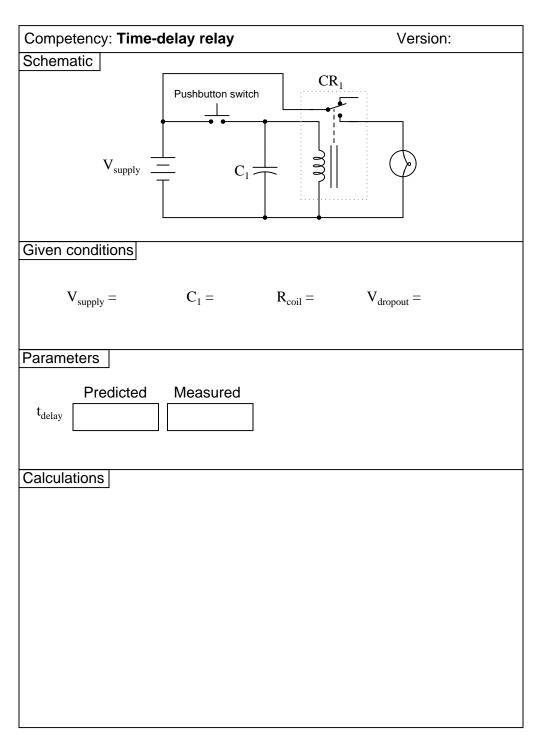
<u>file 01652</u>

Competency: Parallel capacitances	Version:
Schematic Schematic	V 6151011.
C <sub>1</sub> C <sub>2</sub> C <sub>3</sub>	
Given conditions	
$C_1 = C_2 = C_3$	$C_3 =$
Parameters	
Predicted Measured  C <sub>total</sub>	
Analysis Equation used to calculate C <sub>total</sub>	:

 $\underline{\mathrm{file}\ 01653}$ 

	: RC discharge cir	cuit		Version:
Schematic	Pushbutto  V <sub>supply</sub>	n switch	$R_1$	V Meter
Given conditi	ons			
	$V_{\text{supply}} =$	$C_1 =$		$R_1 =$
	$t_1 =$	$t_2 =$		t <sub>3</sub> =
Parameters				
$egin{array}{c c} V_{t1} & & & \\ V_{t2} & & & \\ V_{t3} & & & \\ \hline \end{array}$	dicted Measured	d		
Calculations				

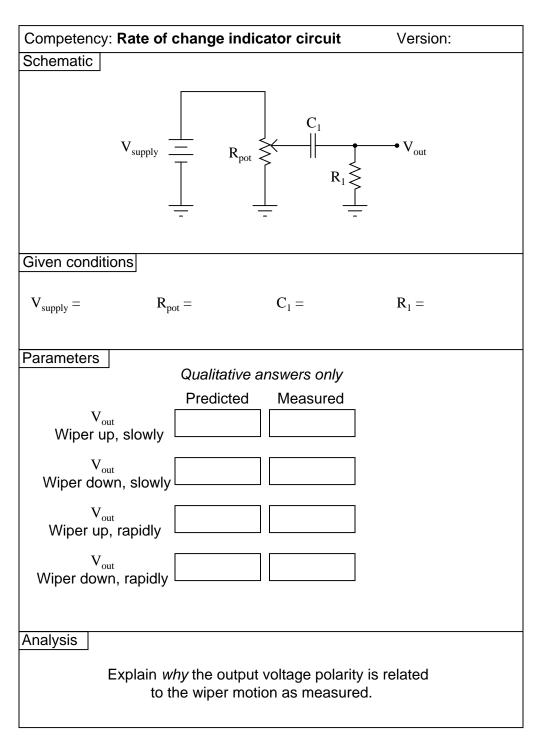
 $\underline{\mathrm{file}\ 01648}$ 



<u>file 01647</u>

Competency	: RC charge/discl	harge circuit	V	/ersion:
Schematic				
	V <sub>supply</sub>	Switch R	$C_1$	Meter
Given conditi	ons			
	$V_{\text{supply}} =$	$C_1 =$	$R_1$	=
	$\mathbf{t}_1 =$	$t_2 =$	$t_3$	=
Parameters				
Chargi	ing from 0 volts		Discharging i	from $V_{supply}$
Pred	icted Measured	<u>d</u>	Predicted	Measured
V <sub>t1</sub>		$V_{t1}$		
$V_{t2}$		$V_{t2}$		
V <sub>t3</sub>		$V_{t3}$		
Calculations				

 $\underline{\mathrm{file}\ 01657}$ 



file 03178

# (Template)

Competency:	Version:
Schematic	
Given conditions	
orren senamene	
Doromotoro	
Parameters	
Predicted Measured	

<u>file 01602</u>

#### Answers

#### Answer 1

Use circuit simulation software to verify your predicted and measured parameter values.

#### Answer 2

Use circuit simulation software to verify your predicted and measured parameter values.

#### Answer 3

Use circuit simulation software to verify your predicted and measured parameter values.

#### Answer 4

Use circuit simulation software to verify your predicted and measured parameter values.

#### Answer 5

Use circuit simulation software to verify your predicted and measured parameter values.

#### Answer 6

Use circuit simulation software to verify your predicted and measured parameter values.

#### Answer 7

Use circuit simulation software to verify your predicted and measured parameter values.

#### Answer 8

Use circuit simulation software to verify your predicted and measured parameter values.

#### Answer 9

Use circuit simulation software to verify your predicted and measured parameter values.

#### Answer 10

Use circuit simulation software to verify your predicted and measured parameter values.

#### Answer 11

Use circuit simulation software to verify your predicted and measured parameter values.

#### Answer 12

Use circuit simulation software to verify your predicted and measured parameter values.

#### Answer 13

Use circuit simulation software to verify your predicted and measured parameter values.

#### Answer 14

Use circuit simulation software to verify your predicted and measured parameter values.

#### Answer 15

Use circuit simulation software to verify your predicted and measured parameter values.

#### Answer 16

Use circuit simulation software to verify your predicted and measured parameter values.

#### Answer 17

Use circuit simulation software to verify your predicted and measured parameter values.

#### Answer 18

The ohmmeter's indication is the "final word" on resistance.

#### Answer 19

Use circuit simulation software to verify your predicted and measured parameter values.

#### Answer 20

Use circuit simulation software to verify your predicted and measured parameter values.

#### Answer 21

The neon bulb will likely give you more reliable confirmation of your predictions than simulation software.

#### Answer 22

Use circuit simulation software to verify your predicted and measured parameter values.

#### Answer 23

Use circuit simulation software to verify your predicted and measured parameter values.

#### Answer 24

Use circuit simulation software to verify your predicted and measured parameter values.

You might be surprised to find that  $L_{total} \neq L_1 + L_2$ . This is due to the *mutual inductance* between inductors  $L_1$  and  $L_2$ .

#### Answer 25

Use circuit simulation software to verify your predicted and measured parameter values.

#### Answer 26

Use circuit simulation software to verify your predicted and measured parameter values.

#### Answer 27

Use circuit simulation software to verify your predicted and measured parameter values.

#### Answer 28

Use circuit simulation software to verify your predicted and measured parameter values.

#### Answer 29

Use circuit simulation software to verify your predicted and measured parameter values.

#### Answer 30

Use circuit simulation software to verify your predicted and measured parameter values.

#### Answer 31

Here, you would indicate where or how to obtain answers for the requested parameters, but not actually give the figures. My stock answer here is "use circuit simulation software" (Spice, Multisim, etc.).

#### Notes 1

Use a variable-voltage, regulated power supply to supply any amount of DC voltage below 30 volts. Students will have to choose resistor values appropriate to the task.

An extension of this exercise is to incorporate troubleshooting questions. Whether using this exercise as a performance assessment or simply as a concept-building lab, you might want to follow up your students' results by asking them to predict the consequences of certain circuit faults.

#### Notes 2

Use a variable-current, regulated power supply to supply any amount of DC current below a few milliamps. Students will have to choose resistor values appropriate to the task. I recommend low-value resistors so as to keep the voltage drop (and power dissipation!) low.

An extension of this exercise is to incorporate troubleshooting questions. Whether using this exercise as a performance assessment or simply as a concept-building lab, you might want to follow up your students' results by asking them to predict the consequences of certain circuit faults.

#### Notes 3

Use a variable-voltage, regulated power supply to supply any amount of DC voltage below 30 volts. Specify standard resistor values, all between 1 k $\Omega$  and 100 k $\Omega$  (1k5, 2k2, 2k7, 3k3, 4k7, 5k1, 6k8, 10k, 22k, 33k, 39k 47k, 68k, etc.).

An extension of this exercise is to incorporate troubleshooting questions. Whether using this exercise as a performance assessment or simply as a concept-building lab, you might want to follow up your students' results by asking them to predict the consequences of certain circuit faults.

#### Notes 4

Use a variable-voltage, regulated power supply to supply any amount of DC voltage below 30 volts. Specify standard resistor values, all between 1 k $\Omega$  and 100 k $\Omega$  (1k5, 2k2, 2k7, 3k3, 4k7, 5k1, 6k8, 10k, 22k, 33k, 39k 47k, 68k, etc.).

An extension of this exercise is to incorporate troubleshooting questions. Whether using this exercise as a performance assessment or simply as a concept-building lab, you might want to follow up your students' results by asking them to predict the consequences of certain circuit faults.

#### Notes 5

Use a variable-voltage, regulated power supply to supply any amount of DC voltage below 30 volts. Specify standard resistor values, all between 1 k $\Omega$  and 100 k $\Omega$  (1k5, 2k2, 2k7, 3k3, 4k7, 5k1, 6k8, 10k, 22k, 33k, 39k 47k, 68k, etc.).

An extension of this exercise is to incorporate troubleshooting questions. Whether using this exercise as a performance assessment or simply as a concept-building lab, you might want to follow up your students' results by asking them to predict the consequences of certain circuit faults.

#### Notes 6

Use a variable-voltage, regulated power supply to supply any amount of DC voltage below 30 volts. Specify standard resistor values, all between 1 k $\Omega$  and 100 k $\Omega$  (1k5, 2k2, 2k7, 3k3, 4k7, 5k1, 6k8, 10k, 22k, 33k, 39k 47k, 68k, etc.).

An extension of this exercise is to incorporate troubleshooting questions. Whether using this exercise as a performance assessment or simply as a concept-building lab, you might want to follow up your students' results by asking them to predict the consequences of certain circuit faults.

Use a variable-voltage, regulated power supply to supply any amount of DC voltage below 30 volts. Specify standard resistor values, all between 1 k $\Omega$  and 100 k $\Omega$  (1k5, 2k2, 2k7, 3k3, 4k7, 5k1, 6k8, 8k2, 10k, 22k, 33k, 39k 47k, 68k, 82k, etc.).

An extension of this exercise is to incorporate troubleshooting questions. Whether using this exercise as a performance assessment or simply as a concept-building lab, you might want to follow up your students' results by asking them to predict the consequences of certain circuit faults.

#### Notes 8

Use a variable-voltage, regulated power supply to supply any amount of DC voltage below 30 volts. Specify standard resistor values, all between 1 k $\Omega$  and 100 k $\Omega$  (1k5, 2k2, 2k7, 3k3, 4k7, 5k1, 6k8, 8k2, 10k, 22k, 33k, 39k 47k, 68k, 82k, etc.).

An extension of this exercise is to incorporate troubleshooting questions. Whether using this exercise as a performance assessment or simply as a concept-building lab, you might want to follow up your students' results by asking them to predict the consequences of certain circuit faults.

#### Notes 9

Use a variable-voltage, regulated power supply to supply any amount of DC voltage below 30 volts. Specify standard resistor values, all between 1 k $\Omega$  and 100 k $\Omega$  (1k5, 2k2, 2k7, 3k3, 4k7, 5k1, 6k8, 8k2, 10k, 22k, 33k, 39k 47k, 68k, 82k, etc.).

An extension of this exercise is to incorporate troubleshooting questions. Whether using this exercise as a performance assessment or simply as a concept-building lab, you might want to follow up your students' results by asking them to predict the consequences of certain circuit faults.

#### General concepts and principles to emphasize

- Voltage is a differential quantity: always measured between two points
- Continuous current must be the same through all series-connected components
- Voltage must be the same across all parallel-connected components
- The distinction between electrical sources and electrical loads
- Qualitative analysis using Ohm's Law (i.e. how to analyze increases and decreases without the use of numerical quantities)
- Calculating the total resistance of a series-parallel resistor network

- Sketch the direction of current in this circuit. Also sketch the + and symbols marking voltage polarity across each component in the circuit. Explain why the relationship between the power supply's voltage polarity and current direction is different than the relationship between each resistors' voltage polarity and current direction. Identify whether each of the components in this circuit is an electrical *source* or an electrical *load*.
- If just one of these resistor's resistance happens to increase, how will this change affect all the other electrical quantities in the circuit?
- If just one of these resistor's resistance happens to decrease, how will this change affect all the other electrical quantities in the circuit?
- If the power supply's voltage happens to increase, how will this change affect all the other electrical quantities in the circuit?

Be sure to remind your students that resistances  $R_1$  and  $R_2$  may need to be series-parallel networks in themselves, to achieve the necessary values. An alternative you may wish to permit is the use of 10-turn (precision) potentiometers connected as rheostats for  $R_1$  and  $R_2$ . This way the circuit's minimum and maximum values may be precisely calibrated. The main potentiometer,  $R_{pot1}$ , should be a 3/4 turn unit, to allow fast checking of minimum and maximum total resistance, and it should be some common value such as 1 k $\Omega$  or 10 k $\Omega$ .

#### Notes 11

Students need not measure potentiometer shaft angles in order to do this exercise. Rather, all they need to do is measure resistance between the wiper and the two outer terminals to set the potentiometer to a position where it will produce the specified division of voltage.

An extension of this exercise is to incorporate troubleshooting questions. Whether using this exercise as a performance assessment or simply as a concept-building lab, you might want to follow up your students' results by asking them to predict the consequences of certain circuit faults.

#### Notes 12

Kirchhoff's Voltage Law ("KVL") is one of the fundamental "first principles" of electric circuit analysis. It is imperative that students master this principle and apply it regularly to the circuits they encounter. Not only is KVL useful for quantitative circuit analysis, but also for determining qualitative changes in circuits (e.g. analyzing faults in circuits, as part of circuit troubleshooting).

An extension of this exercise is to incorporate troubleshooting questions. Whether using this exercise as a performance assessment or simply as a concept-building lab, you might want to follow up your students' results by asking them to predict the consequences of certain circuit faults.

## General concepts and principles to emphasize

- Voltage is a differential quantity: always measured between two points
- Kirchhoff's Voltage Law: the algebraic sum of all voltages in any loop is equal to zero
- The relationship between the mathematical sign of the measured voltage (on the DMM display) versus the orientation of the red and black test leads

- Demonstrate Kirchhoff's Voltage Law by using your voltmeter to "step around" any arbitrary loop in this circuit, summing all the voltage readings to show that they algebraically add to zero. For example, stepping around loop **ABCA**:
  - $\rightarrow$  Measure voltage between **B** (red lead) and **A** (black lead)
  - $\rightarrow$  Measure voltage between **C** (red lead) and **B** (black lead)
  - $\rightarrow$  Measure voltage between **A** (red lead) and **C** (black lead)
- You will notice that the given conditions in this circuit call for equal-valued resistors, as well as a power supply voltage that is evenly divisible by four. Explain why these conditions make the circuit easier to analyze.

I recommend students use a normal regulated (voltage) power supply, adjusting the output voltage until the output current is at 4 mA. 1 k $\Omega$  resistors work well for this circuit, requiring only 6.4 volts from the power supply to achieve 4 mA total current.

An extension of this exercise is to incorporate troubleshooting questions. Whether using this exercise as a performance assessment or simply as a concept-building lab, you might want to follow up your students' results by asking them to predict the consequences of certain circuit faults.

#### Notes 14

Use a variable-voltage, regulated power supply to supply any amount of DC voltage below 30 volts. Specify standard resistor values, all between 1 k $\Omega$  and 100 k $\Omega$  (1k5, 2k2, 2k7, 3k3, 4k7, 5k1, 6k8, 8k2, 10k, 22k, 33k, 39k 47k, 68k, 82k, etc.).

I have used this circuit as both a "quick" lab exercise and a troubleshooting exercise, using values of  $10 \text{ k}\Omega$  for R1, R2, and R3;  $15 \text{ k}\Omega$  for R(load1);  $22 \text{ k}\Omega$  for R(load2); and 6 volts for the power supply. Of course, these component values are not critical, but they do provide easy-to measure voltages and currents without incurring excessive impedances that would cause significant voltmeter loading problems.

An extension of this exercise is to incorporate troubleshooting questions. Whether using this exercise as a performance assessment or simply as a concept-building lab, you might want to follow up your students' results by asking them to predict the consequences of certain circuit faults.

#### General concepts and principles to emphasize

- Voltage is a differential quantity: always measured between two points
- Continuous current must be the same through all series-connected components
- Voltage must be the same across all parallel-connected components
- The distinction between electrical sources and electrical loads
- Qualitative analysis using Ohm's Law (i.e. how to analyze *increases* and *decreases* without the use of numerical quantities)

- Sketch the direction of current in this circuit. Also sketch the + and symbols marking voltage polarity across each component in the circuit. Explain why the relationship between the power supply's voltage polarity and current direction is different than the relationship between each resistors' voltage polarity and current direction. Identify whether each of the components in this circuit is an electrical *source* or an electrical *load*.
- If just one of the load resistor's resistance happens to increase, how will this change affect the voltage seen by the other load?
- If just one of the voltage divider resistor's resistance happens to increase, how will this change affect the voltage seen by the two loads?
- If the power supply's voltage happens to increase, how will this change affect all the other electrical quantities in the circuit?
- Not all voltage divider circuits behave the same when loaded. For example two different divider circuits, each one dividing the source voltage into thirds (i.e. 66%, 33%), may yield the exact same output voltages with no loads attached, but as soon as equal loads are attached to them their output voltages "sag" to different degrees. Explain why this happens, and what is it about the more "stout" voltage divider circuit that allows it to better maintain its ideal output voltage under load.

Use a variable-voltage, regulated power supply to supply any amount of DC voltage below 30 volts. Specify standard resistor values, all between 1 k $\Omega$  and 100 k $\Omega$  (1k5, 2k2, 2k7, 3k3, 4k7, 5k1, 6k8, 10k, 22k, 33k, 39k 47k, 68k, etc.).

An extension of this exercise is to incorporate troubleshooting questions. Whether using this exercise as a performance assessment or simply as a concept-building lab, you might want to follow up your students' results by asking them to predict the consequences of certain circuit faults.

#### Notes 16

Students need not measure potentiometer shaft angles in order to do this exercise. Rather, all they need to do is measure resistance between the wiper and the two outer terminals to set the potentiometer to a position where it will produce the specified division of voltage.

 $R_{pot}$  refers to the potentiometer's nominal full-range value (for example, 1 k $\Omega$  or 5 k $\Omega$ ), and not to its particular setting. The setting is what the student must figure out to achieve  $V_{out}$ .

An extension of this exercise is to incorporate troubleshooting questions. Whether using this exercise as a performance assessment or simply as a concept-building lab, you might want to follow up your students' results by asking them to predict the consequences of certain circuit faults.

Use a variable-voltage, regulated power supply to supply any amount of DC voltage below 30 volts. Specify standard resistor values, all between 1 k $\Omega$  and 100 k $\Omega$  (1k5, 2k2, 2k7, 3k3, 4k7, 5k1, 6k8, 10k, 22k, 33k, 39k 47k, 68k, etc.), and be sure to specify a potentiometer value in excess of the amount required to balance the bridge.

An extension of this exercise is to incorporate troubleshooting questions. Whether using this exercise as a performance assessment or simply as a concept-building lab, you might want to follow up your students' results by asking them to predict the consequences of certain circuit faults.

## General concepts and principles to emphasize

- Voltage is a differential quantity: always measured between two points
- Continuous current must be the same through all series-connected components
- Voltage must be the same across all parallel-connected components
- The concept of "balancing" a Wheatstone bridge circuit
- The relationship between the mathematical sign of the measured voltage (on the DMM display) versus the orientation of the red and black test leads

#### Suggestions for Socratic discussion and experimentation

- Wheatstone bridge circuits have traditionally been used to measure unknown resistances, by comparing them against known resistances. This is analogous to a laboratory balance-scale, where a sample of unknown weights is compared against a set of brass cylinders of known weight, the beam achieving equilibrium only when the sum total of the brass weights equals the sample weight. Explain how you could use a Wheatstone bridge circuit in the same way, given a collection of precision resistors of known value
- How would the operation of this bridge circuit be affected if the potentiometer were relocated to a different "arm" of the bridge?
- The meter symbol in the center of the bridge (marked with the letter "G") is supposed to represent a galvanometer: a highly sensitive electrical meter. Demonstrate how to set up your multimeter to be as sensitive as possible for use as a galvanometer.
- A galvanometer used in a Wheatstone bridge circuit only has to be accurate at one point in its measurement range: zero. Explain why accuracy at other points along the galvanometer's range is irrelevant.

#### Notes 18

Use a variable-voltage, regulated power supply to supply any amount of DC voltage below 30 volts. Use precision resistors for  $R_1$  and  $R_2$ , and use any standard resistor value for  $R_x$  between 1 k $\Omega$  and 100 k $\Omega$ .

An extension of this exercise is to incorporate troubleshooting questions. Whether using this exercise as a performance assessment or simply as a concept-building lab, you might want to follow up your students' results by asking them to predict the consequences of certain circuit faults.

Students may use potentiometers in their range resistance networks to achieve precise values. However, they are not allowed to adjust those potentiometers after connecting them to the meter movement – they must set their potentiometer(s) during the "prediction" step of the assessment before the circuit is completely built.

An extension of this exercise is to incorporate troubleshooting questions. Whether using this exercise as a performance assessment or simply as a concept-building lab, you might want to follow up your students' results by asking them to predict the consequences of certain circuit faults.

#### Notes 20

Be sure to specify resistor values for the voltage divider that will show a marked impact when measured with the type of voltmeter you expect your students to use. If you size the resistors for a modest impact measured with an analog voltmeter (20,000  $\Omega$ /Volt), your students may not see much of an impact when using a modern digital voltmeter ( $Z_{in} > 10 \text{ M}\Omega$ ).

New students often have a difficult time grasping the main idea of this activity, due to the assumption of the voltmeter's indication always being taken as true. The purpose of this activity is to shatter that assumption: to teach students that electrical measurements are never truly passive – rather, they invariably impact the circuit being measured in some way. Usually, the impact is so small it may be safely ignored. Here, due to the large resistor values used in the divider circuit, the impact of voltmeter usage on the circuit is non-trivial.

Another aspect of this activity that escapes some students' attention is that the circuit must be analyzed twice: once with the meter connected and once without. The point here is that the meter becomes a component of the circuit when it is connected across  $R_2$ , and thus changes all the voltages and currents.

#### Notes 21

Students may either use ready-made inductors for this experiment (the larger the value, the more impressive the light flash!) or inductors of their own making (using old solenoid valve coils, or hand-wound coils around steel bolts). Power transformer primary windings also work well for this.

You will need an inductance meter in your lab to do this exercise. If you don't have one, you should get one right away!

#### General concepts and principles to emphasize

- Self-induction (i.e. how a single coil is able to experience induction as a consequence of a changing current passing through it)
- Faraday's Law of electromagnetic induction:  $E = N \frac{d\phi}{dt}$
- Lenz's Law (that the direction of induced current tends to oppose the instigating change)
- The relationship between inductor coil geometry, core material, and inductance

- Identify ways to modify each inductor so as to create more total inductance.
- Explain why total inductance relates to individual inductor values, basing your explanation strictly on an analysis of voltages and currents in this network.
- Suppose these inductors were built such that their magnetic fields were not contained within their structures, but rather were "open" and could thereby interact with the magnetic fields from other inductors. What would happen if we placed two of these series-connected inductors close together so that their magnetic fields opposed each other? What would happen if we placed two of these series-connected inductors close together so that their magnetic fields aided each other?
- A technique used in making wire-wound resistors is something called a *bifilar winding*. Bifilar windings have nearly zero inductance. Research how a bifilar winding is made, and then explain how it works to minimize induction and also why this might be important for making resistors.

You will need an inductance meter in your lab to do this exercise. If you don't have one, you should get one right away!

#### General concepts and principles to emphasize

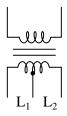
- Self-induction (i.e. how a single coil is able to experience induction as a consequence of a changing current passing through it)
- Faraday's Law of electromagnetic induction:  $E = N \frac{d\phi}{dt}$
- Lenz's Law (that the direction of induced current tends to oppose the instigating change)
- The relationship between inductor coil geometry, core material, and inductance

#### Suggestions for Socratic discussion and experimentation

- Identify ways to modify each inductor so as to create more total inductance.
- Explain *why* total inductance relates to individual inductor values, basing your explanation strictly on an analysis of voltages and currents in this network.
- Suppose these inductors were built such that their magnetic fields were not contained within their structures, but rather were "open" and could thereby interact with the magnetic fields from other inductors. What would happen if we placed two of these series-connected inductors close together so that their magnetic fields opposed each other? What would happen if we placed two of these series-connected inductors close together so that their magnetic fields aided each other?
- A technique used in making wire-wound resistors is something called a *bifilar winding*. Bifilar windings have nearly zero inductance. Research how a bifilar winding is made, and then explain how it works to minimize induction and also why this might be important for making resistors.

#### Notes 24

In case students don't have access to a pair of inductors on a common core, they may either make their own by winding wire around a long ferromagnetic core, or use a center-tapped inductor (or transformer winding). The latter solution is probably the easiest:



Inexpensive audio output transformers (with center-tapped 1000  $\Omega$  primary windings) work very well for this. Your students' parts kits should contain at least one of these transformers anyway if they are to do audio coupling experiments later.

You will need an inductance meter in your lab to do this exercise. If you don't have one, you should get one right away!

Many modern digital multimeters come equipped with capacitance measurement built-in. If your students do not have these meters, you will either need to provide one for them to use, or provide an LCR meter. If you don't have either one of these instruments, you should get one right away!

## General concepts and principles to emphasize

• The relationship between capacitor plate geometry, dielectric material, and capacitance

## Suggestions for Socratic discussion and experimentation

- Identify ways to modify each capacitor so as to create more total capacitance.
- Explain why total capacitance relates to individual capacitor values, basing your explanation strictly on an analysis of voltages and currents in this network.

## Notes 26

Many modern digital multimeters come equipped with capacitance measurement built-in. If your students do not have these meters, you will either need to provide one for them to use, or provide an LCR meter. If you don't have either one of these instruments, you should get one right away!

## General concepts and principles to emphasize

The relationship between capacitor plate geometry, dielectric material, and capacitance

- Identify ways to modify each capacitor so as to create more total capacitance.
- Explain why total capacitance relates to individual capacitor values, basing your explanation strictly on an analysis of voltages and currents in this network.

RC charging and discharging circuits are tremendously useful in electronics, being applied to everything from timers to power converters. LR circuits are technically just as useful, but the size, weight, and expense of inductors capable of mimicing the same behavior as readily-available capacitors makes LR circuits less common than RC circuits.

A tremendously important concept in electric circuit analysis is the distinction between a *source* and a *load*. Reactive components such as capacitors and inductors have the unique ability to act as both, depending on the conditions impressed upon these components. In some situations, a capacitor or inductor may absorb energy from the circuit (i.e. act as a load). In other situations, a capacitor or inductor may release energy into the circuit (i.e. act as a source).

I recommend choosing resistor and capacitor values that yield time constants in the range that may be accurately tracked with a stopwatch. I also recommend using resistor values significantly less than the voltmeter's input impedance, so that voltmeter loading does not significantly contribute to the decay rate.

Good time values to use  $(t_1, t_2, t_3)$  would be in the range of 5, 10, and 15 seconds, respectively.

An extension of this exercise is to incorporate troubleshooting questions. Whether using this exercise as a performance assessment or simply as a concept-building lab, you might want to follow up your students' results by asking them to predict the consequences of certain circuit faults.

## General concepts and principles to emphasize

- How to manipulate algebraic equations containing exponential terms
- The distinction between electrical sources and electrical loads

- Identify multiple ways to modify this circuit so as to achieve a longer (slower) discharge time.
- Identify multiple ways to modify this circuit so as to achieve a shorter (faster) discharge time.
- Modify this circuit to make the *charging* time slow as well.
- Sketch the direction of current in this circuit while the pushbutton switch is being pressed. Also sketch the + and symbols marking voltage polarity across each component in the circuit. Identify whether each of the components in this circuit is an electrical *source* or an electrical *load* during this time period.
- Sketch the direction of current in this circuit while the pushbutton switch is not being pressed. Also sketch the + and symbols marking voltage polarity across each component in the circuit. Identify whether each of the components in this circuit is an electrical *source* or an electrical *load* during this time period.

Two very important "given" parameters are the relay coil resistance  $(R_{coil})$  and the relay dropout voltage  $(V_{dropout})$ . These are best determined experimentally.

Many students fail to grasp the purpose of this exercise until it is explained. The idea here is to predict when the relay will "drop out" after the switch is opened. This means solving for t in the time-constant (decay) equation given the initial capacitor voltage, time constant  $(\tau)$ , and the capacitor voltage at time t. Because this involves the use of logarithms, students may be perplexed until given assistance.

An extension of this exercise is to incorporate troubleshooting questions. Whether using this exercise as a performance assessment or simply as a concept-building lab, you might want to follow up your students' results by asking them to predict the consequences of certain circuit faults.

#### General concepts and principles to emphasize

- The purpose for and operation of an electromechanical relay
- RC time-constant calculations

#### Suggestions for Socratic discussion and experimentation

- Identify at least one way to modify this circuit so as to achieve a longer time delay to the relay's operation.
- Identify at least one way to modify this circuit so as to achieve a shorter time delay to the relay's operation.
- Is it possible to achieve a similar time-delay effect without using a relay at all?
- Sketch the direction of current in this circuit while the pushbutton switch is being pressed. Also sketch the + and symbols marking voltage polarity across each component in the circuit. Identify whether each of the components in this circuit is an electrical *source* or an electrical *load* during this time period.
- Sketch the direction of current in this circuit while the pushbutton switch is not being pressed. Also sketch the + and symbols marking voltage polarity across each component in the circuit. Identify whether each of the components in this circuit is an electrical *source* or an electrical *load* during this time period.

#### Notes 29

I recommend choosing resistor and capacitor values that yield time constants in the range that may be accurately tracked with a stopwatch. I also recommend using resistor values significantly less than the voltmeter's input impedance, so that voltmeter loading does not significantly contribute to the decay rate.

Good time values to use  $(t_1, t_2, t_3)$  would be in the range of 5, 10, and 15 seconds, respectively.

An extension of this exercise is to incorporate troubleshooting questions. Whether using this exercise as a performance assessment or simply as a concept-building lab, you might want to follow up your students' results by asking them to predict the consequences of certain circuit faults.

### Notes 30

I recommend a supply voltage of 12 volts, a potentiometer value of 10 k $\Omega$ , a capacitor value of 0.1  $\mu$ F, and a loading resistor ( $R_1$ ) of 1 M $\Omega$ . Use a DMM so as to not load the circuit any more than necessary. If you wish to choose different capacitor/resistor values, I strongly suggest choosing them such that the time constant ( $\tau$ ) of the circuit significantly faster than 1 second.

An extension of this exercise is to incorporate troubleshooting questions. Whether using this exercise as a performance assessment or simply as a concept-building lab, you might want to follow up your students' results by asking them to predict the consequences of certain circuit faults.

Any relevant notes for the assessment activity go here.