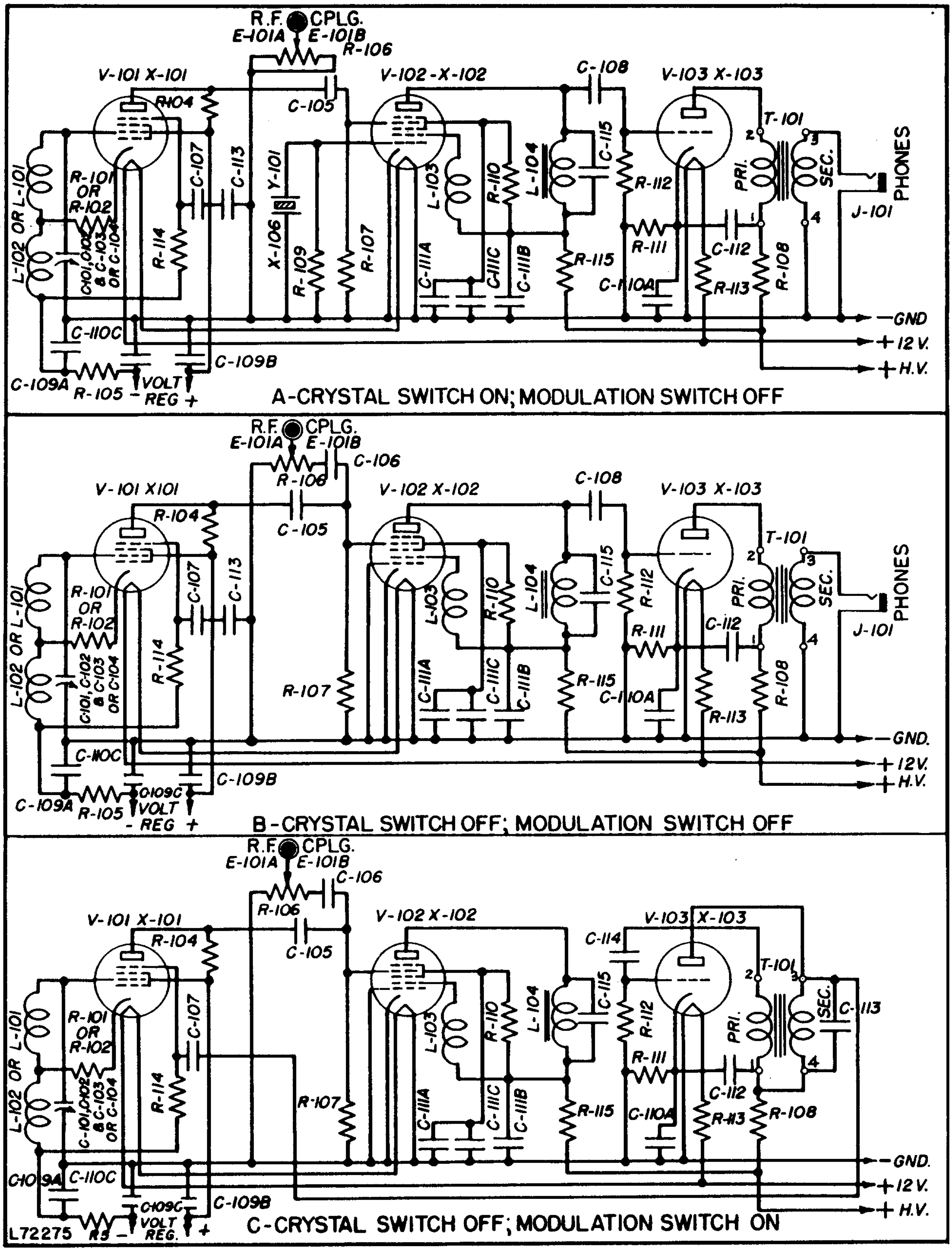

Figure 7.1. Fundamental Circuits.