

the agency prior to award. The decision of the Grants Officer is final.

**Evaluation Criteria:** For the SURF Programs, the evaluation criteria are:

Evaluation of student's academic ability and commitment to program goals: includes evaluation of the following: completed course work; expressed research interest; compatibility of the expressed research interest with SURF Program research areas; research skills; grade point average in courses relevant to the SURF Program; career goals; honors and activities.

Evaluation of applicant institution's commitment to program goals: includes evaluation of the following: the institution's academic department(s) relevant to the discipline(s) of the student(s).

Each of these factors is given equal weight in the evaluation process.

**Award Period:** The SURF Programs are anticipated to run from May 27 through August 15, 2003; adjustments may be made to accommodate specific academic schedules (e.g., a limited number of 9-week cooperative agreements).

**Matching Requirements:** The SURF Programs do not require any matching funds.

**Application Kit:** For the EEEL, MEL, CSTL, PL, MSEL, BFRL, and ITL SURF Programs, an application kit, containing all required forms and certifications, may be obtained by contacting Ms. Anita Sweigert, (301) 975-4200; websites for each program's application kit may be accessed through the following Web site: <http://www.surf.nist.gov/surf2.htm>.

### **Electronics and Electrical Engineering Laboratory (EEEL) Grants Program**

**Dates:** The Electronics and Electrical Engineering Laboratory Grants Program proposals must be received no later than the close of business September 30, 2003. Proposals received after June 30, 2003, will continue to be processed and considered for funding but may be funded in the next fiscal year, subject to the availability of funds.

**Addresses:** For the Electronics and Electrical Engineering Laboratory Grants Program, submit one signed original and two copies of the proposal package to: Electronics and Electrical Engineering Laboratory, Attn.: Sheila Bryner, National Institute of Standards and Technology, 100 Bureau Drive, Stop 8100, Gaithersburg, MD 20899-8100. Tel.: (301) 975-2220. Fax: (301) 975-4091.

**Authority:** As authorized by 15 U.S.C. 272(b) and (c), the NIST Electronics and Electrical Engineering Laboratory conducts a

basic and applied research program directly and through grants and cooperative agreements to eligible recipients.

**Program Description and Objectives:** The Electronics and Electrical Engineering Laboratory Grants Program solicits proposals in support of the broad program objectives identified below.

The Electronics and Electrical Engineering Laboratory Grants Program supports the formal mission of the Electronics and Electrical Engineering Laboratory, which is to strengthen the U.S. economy and improve the quality of life by providing measurement science and technology, and by advancing standards, primarily for the electronics and electrical industries.

More specifically, the Electronics and Electrical Engineering Laboratory Grants Program solicits proposals to support specific programs in the areas of metrology for semiconductors (including mainstream silicon, power devices, and compound semiconductors), superconductors (including cryoelectronics and bulk superconductors), electronic instrumentation, optoelectronics, magnetics (including bulk magnetic materials and magnetic data storage), video (including flat-panel displays), electronic commerce as applied to electronic products and devices, the transmission and distribution of electrical power, national electrical standards (fundamental, generally quantum-based physical standards), and law enforcement (clothing, communication systems, emergency equipment, investigative aids, protective equipment, security systems, vehicles, speed-measuring equipment, weapons, and analytical techniques and standard reference materials used by the public safety community).

For details on these various activities, please see the Electronics and Electrical Engineering Laboratory Web site at <http://www.eeel.nist.gov>. Note that documents describing the current programs for the five participating technical divisions and two offices are available through the home page.

Technical contacts for these areas are:

#### **Semiconductors**

**Semiconductor Electronics Division—**Division Chief: Dr. David G. Seiler; (301) 975-2054; [david.seiler@nist.gov](mailto:david.seiler@nist.gov).

**Office of Microelectronics Programs—**Director: Dr. Stephen Knight; (301) 975-4400; [stephen.knight@nist.gov](mailto:stephen.knight@nist.gov).

#### **Superconductors (Bulk); Magnetics**

**Magnetic Technology Division—**Division Chief: Dr. Alan F. Clark; (303) 497-5477; [aclark@boulder.nist.gov](mailto:aclark@boulder.nist.gov).

**Superconductors (Cryoelectronics); National Electrical Standards (Josephson Array Development)**

**Electromagnetic Technology Division—**Division Chief: Dr. Richard E. Harris; (303) 497-3678; [richard.harris@boulder.nist.gov](mailto:richard.harris@boulder.nist.gov).

**Electronic Instrumentation; Video; Electronic Commerce; National Electrical Standards (Other Than Josephson Array Development)**

**Electricity Division—**Division Chief: Dr. James K. Olthoff; (301) 975-2400; [james.olthoff@nist.gov](mailto:james.olthoff@nist.gov).

#### **Optoelectronics**

**Optoelectronics Division; Office of Optoelectronics Programs—**Division Chief and Office Director: Dr. Gordon W. Day; (303) 497-5432; [gwday@boulder.nist.gov](mailto:gwday@boulder.nist.gov).

#### **Law enforcement**

**Office of Law Enforcement Standards—**Director: Dr. Kathleen Higgins; (301) 975-2757; [kathleen.higgins@nist.gov](mailto:kathleen.higgins@nist.gov).

**Eligibility:** The Electronics and Electrical Engineering Laboratory Grants Program is open to institutions of higher education; hospitals; non-profit organizations; commercial organizations; State, local, and Indian tribal governments; foreign governments; organizations under the jurisdiction of foreign governments; and international organizations.

**Funding Availability:** Over the past three years, the Electronics and Electrical Engineering laboratory funded a total of approximately \$1,500,000 in grants and cooperative agreements. The amount available each year fluctuates considerably based on programmatic needs. Individual awards are expected to range between \$5,000 and \$150,000.

**Proposal Review Process:** For the Electronics and Electrical Engineering Laboratory Grants Program, proposals will be distributed to the appropriate Division Chief or Office Director or designee based on technical area by one or more technical professionals familiar with the programs of the Electronics and Electrical Engineering Laboratory. The proposals will be reviewed in a two-step process. First, at least three independent, objective individuals knowledgeable about the particular scientific area described in the Program Description and Objectives section above that the proposal addresses will conduct a technical review of each