A V63E causes copying of RADMODES Bit 6 from CH33 Bit 6 (LR position 1 discrete). A V63 with LR power off (e.g. V63 for RR checkout) causes RADMODES Bit 6 to be improperly set to 1. With no subsequent V63's (with LR power on), 522 alarms will result (with no LR data) till Higate in descent (CH33 Bits 6 and 7 are both 1 with LR power off).

First seen by FSB on an LM/CSM descent simulation. 522 alarms occurred once every two seconds until the LGC commanded LR to position II. The LMS does not simulate both position bits; however the current crew checklist if done out of order (being corrected) can cause the same problem (and did) as a result of the position 2 test they do.

**MIT ANALYSIS**

2.1 CAUSE:

See 1.5.

2.2 RECOGNITION:

See 1.6.

2.3 MISSION EFFECT:

No Landing Radar data until Higate is reached.

2.4 AVOIDANCE PROCEDURE:

Do V63E, then V34E, .after LR power is turned on prior to calling P63.

2.5 RECOVERY PROCEDURE:

Do V63E, then V34E in P63. (Not necessary if Avoidance procedure was used.)

2.6 PROGRAM CORRECTION:

See PCR 817.

2.7 RECOMMENDED DISPOSITION (fix, work-around, etc.):

Fixed for LR.

2.8 RECOMMENDED RE-TESTING:

Special Hybrid Test.

3.1 NASA DIRECTION:

CONTINUED ON PAGE

**MIT ANALYSIS**

2.9 MITIL SIGNATURE:

CONTINUED ON PAGE

4.1 CLOSING ACTION TAKEN:

CONTINUED ON PAGE

3.2 NASAMSC SIGNATURE:

3.3 ORGANIZATION:

3.4 DATE:

4.2 SIGNATURE:

4.3 ORGANIZATION:

4.4 DATE: