

## Shift registers

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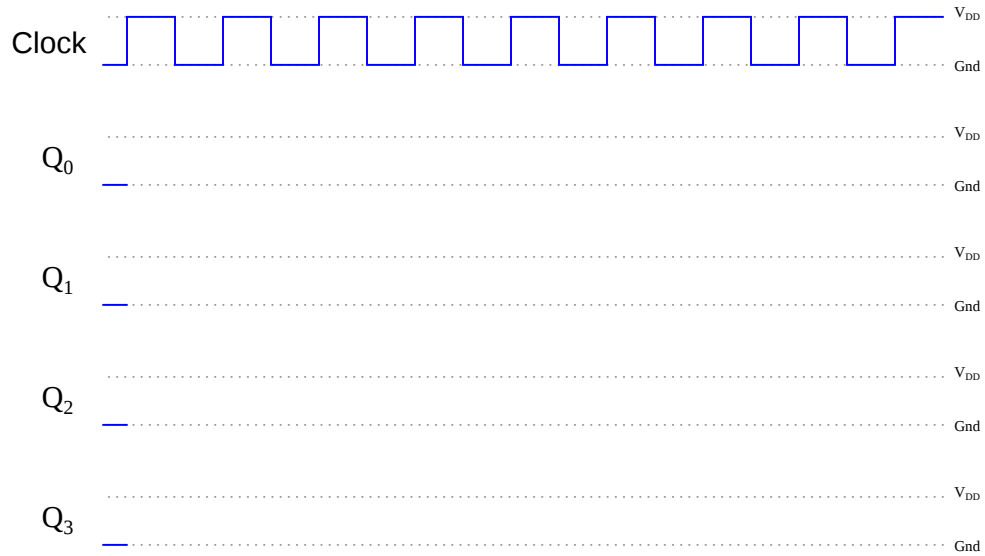
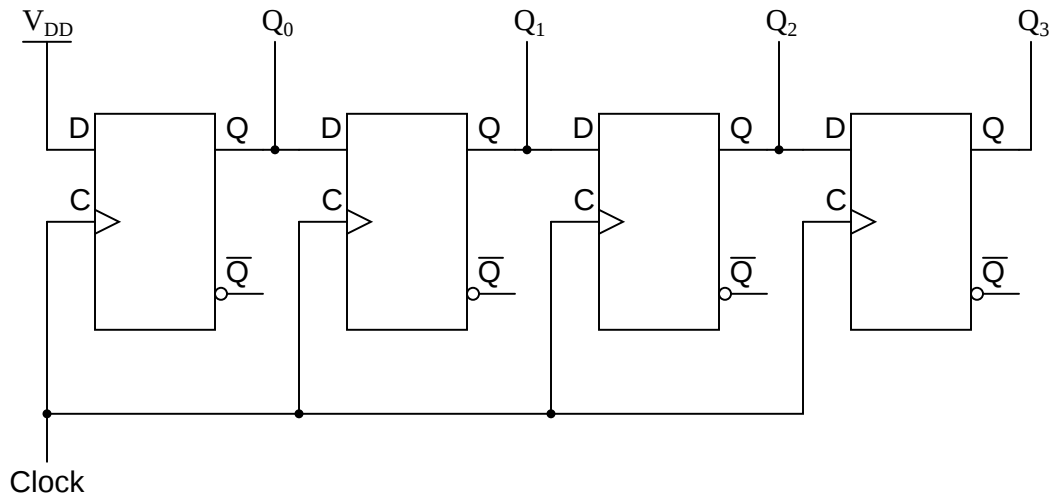
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Resources and methods for learning about these subjects (list a few here, in preparation for your research):

## Questions

### Question 1

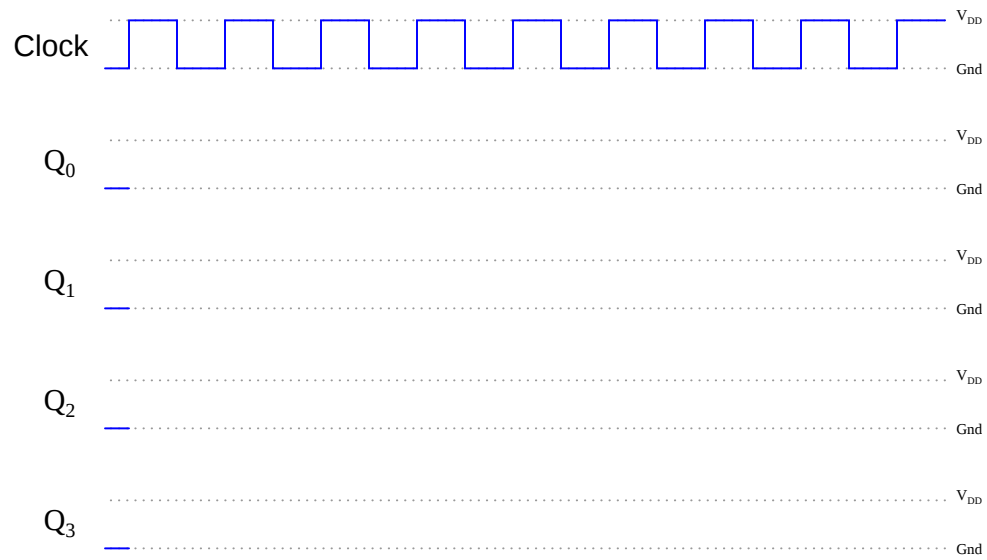
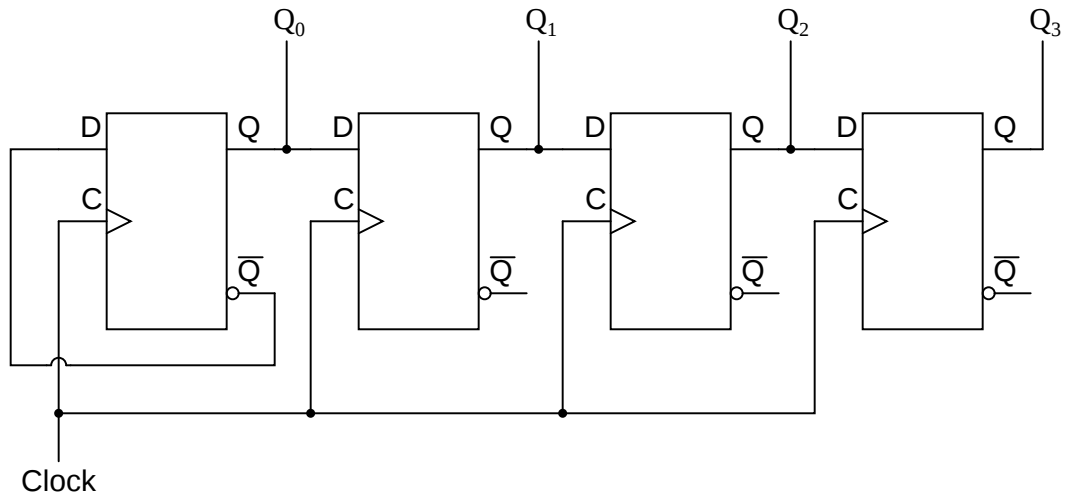
Complete the timing diagram for this circuit, assuming all  $Q$  outputs begin in the low state:



file 02986

## Question 2

Complete the timing diagram for this circuit, assuming all  $Q$  outputs begin in the low state:

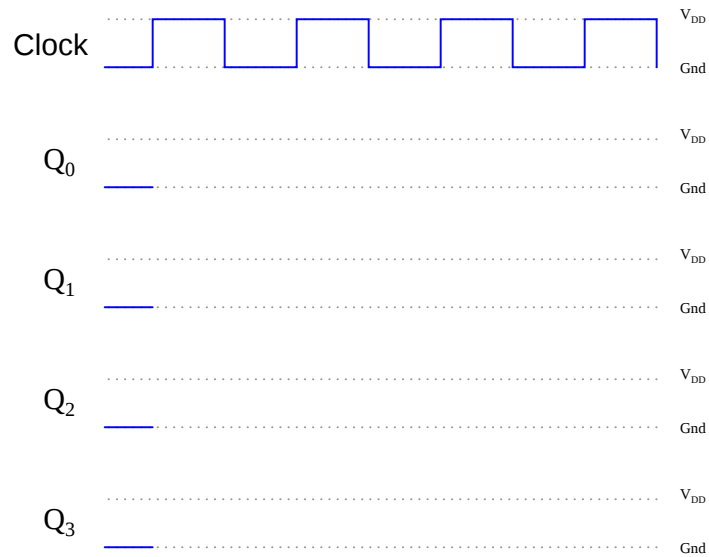
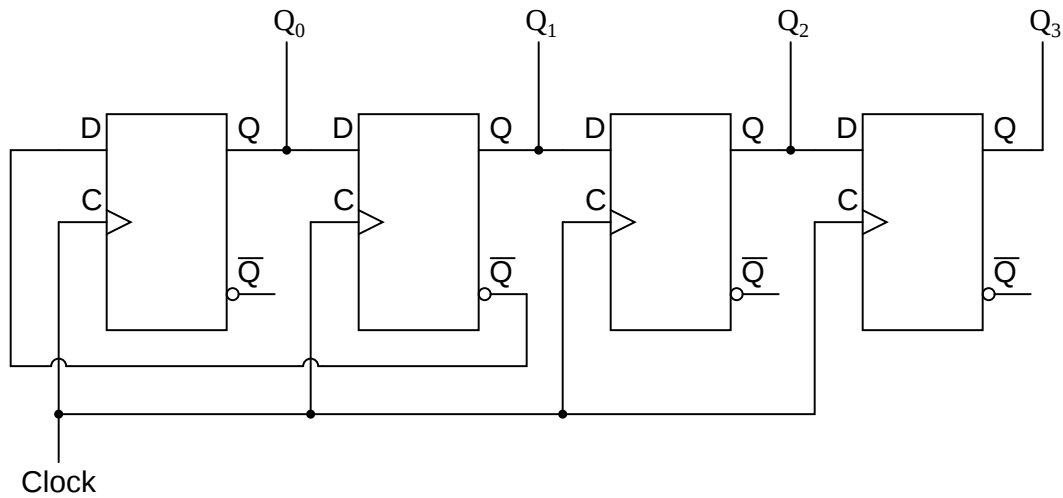


file 02987

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### Question 3

Complete the timing diagram for this circuit, showing propagation delays for all flip-flops (delay times much less than the width of a clock pulse), assuming all  $Q$  outputs begin in the low state:



[file 02988](#)

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### Question 4

What is the definition of a *register* in the context of digital circuitry? Also, define and compare/contrast what a *shift register* is.

[file 02989](#)

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### Question 5

Explain the difference between *serial* digital data and *parallel* digital data.

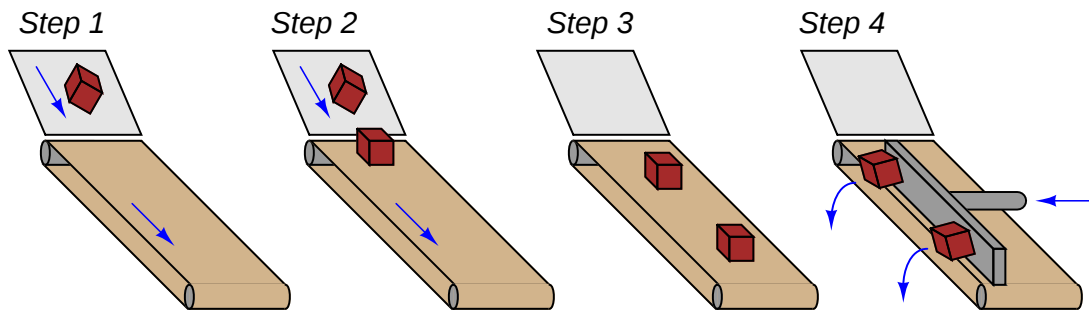
[file 01466](#)

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### Question 6

A helpful analogy for a shift register is a *conveyor belt*. Examine this illustration showing a single conveyor belt at four different times, and determine which of the following shift register operations the sequence represents:

- Parallel-in, serial-out
- Parallel-in, parallel-out
- Serial-in, serial-out
- Serial-in, parallel-out



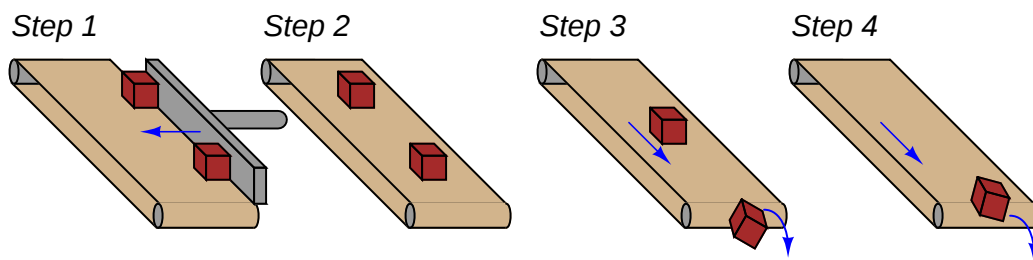
[file 02961](#)

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### Question 7

A helpful analogy for a shift register is a *conveyor belt*. Examine this illustration showing a single conveyor belt at four different times, and determine which of the following shift register operations the sequence represents:

- Parallel-in, serial-out
- Parallel-in, parallel-out
- Serial-in, serial-out
- Serial-in, parallel-out



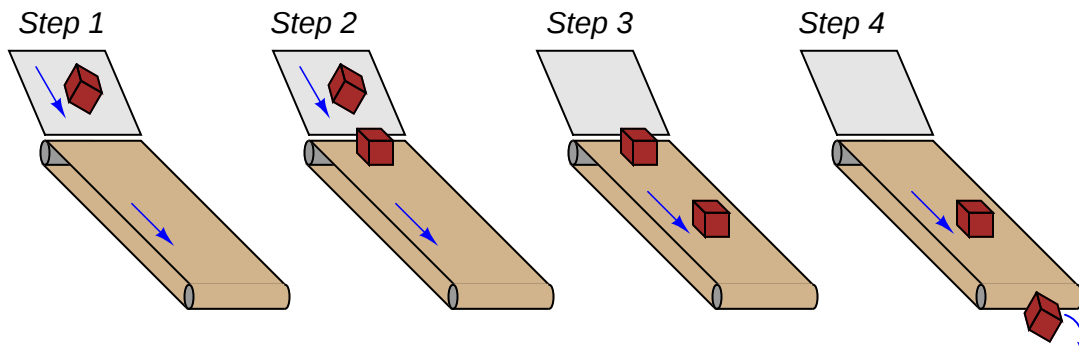
[file 02985](#)

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### Question 8

A helpful analogy for a shift register is a *conveyor belt*. Examine this illustration showing a single conveyor belt at four different times, and determine which of the following shift register operations the sequence represents:

- Parallel-in, serial-out
- Parallel-in, parallel-out
- Serial-in, serial-out
- Serial-in, parallel-out



[file 02960](#)

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### Question 9

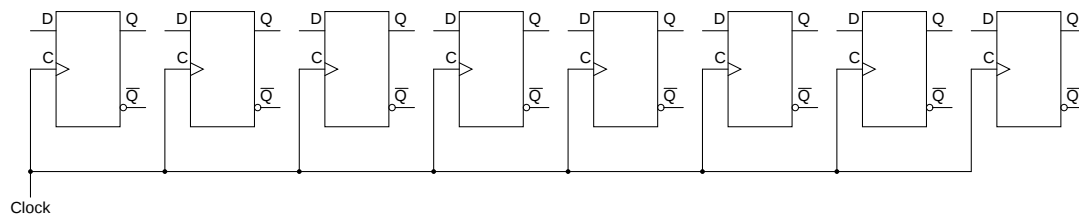
Draw the schematic diagram for a five-bit serial-in/serial-out shift register circuit, and be prepared to give a brief explanation of how it functions.

[file 01467](#)

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### Question 10

Draw the necessary connecting wires between flip-flops so that serial data is shifted from *right to left* instead of left to right as you may be accustomed to seeing in a shift register schematic:



Be sure to also note where data enters this shift register, and where data exits.

[file 01471](#)

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### Question 11

Explain how a shift register circuit could be built from D-type flip-flops with the ability to shift data either to the right or to the left, on command. I'm not necessarily asking for a schematic diagram so much as I'm looking for an *explanation* of how such a circuit might be built. Of course, if your best way of presenting your idea is to draw a schematic diagram, go ahead!

[file 01472](#)

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### Question 12

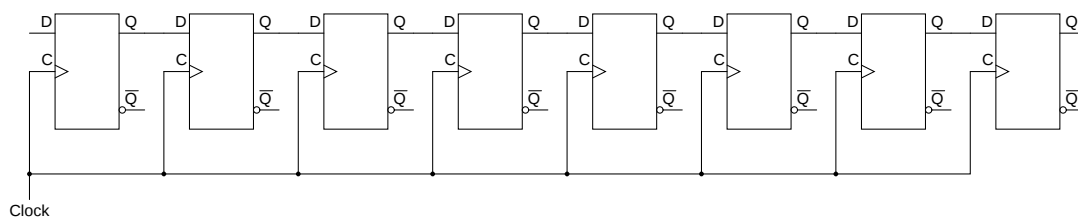
Explain what a *universal* shift register is. The 74194 is an example of a TTL universal shift register, so you will find that datasheet very helpful in answering this question.

[file 01470](#)

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### Question 13

An important function in computer circuitry is *serial-to-parallel data conversion*, where a stream of serial data is "read" one bit at a time, then all bits output at once in parallel form. A shift register circuit is ideal for this application. Shown here is an eight-bit shift register circuit:



Draw any necessary wires and labels showing where serial data would enter the circuit, and where parallel data would exit.

[file 01468](#)

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### Question 14

Suppose we wished to use a shift register circuit to input several binary bits at once (*parallel* data transfer), and then output the bits one at a time over a single line (*serial* data transfer). You should be aware of how shift registers are constructed with D-type flip-flops. Now, describe how we can get parallel data entered into a shift register circuit. Note: there is more than one answer to this question!

[file 01469](#)

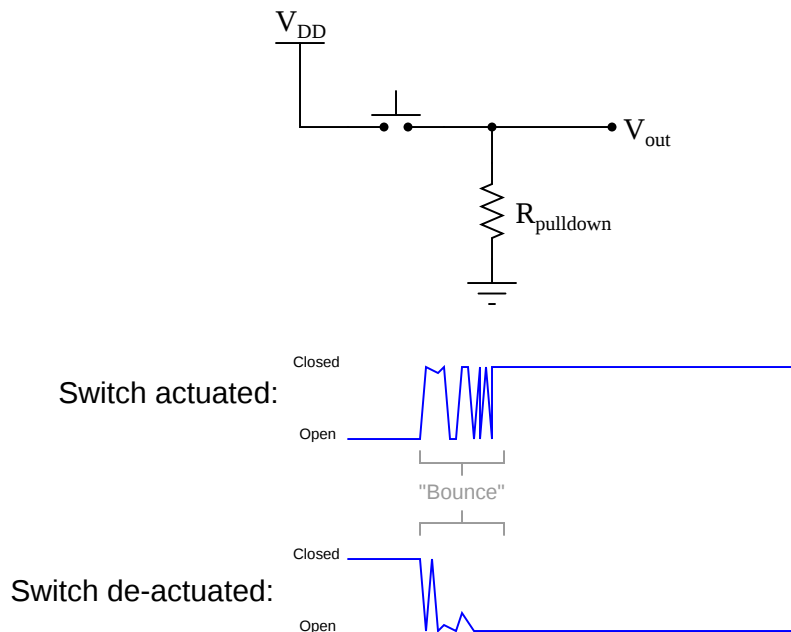
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### Question 15

A binary number is parallel-loaded into a shift register. The shift register is then commanded to "shift right" for one clock pulse. How does the value of the shifted binary number compare to the number originally loaded in, assuming that the MSB is on the very left flip-flop of the shift register?

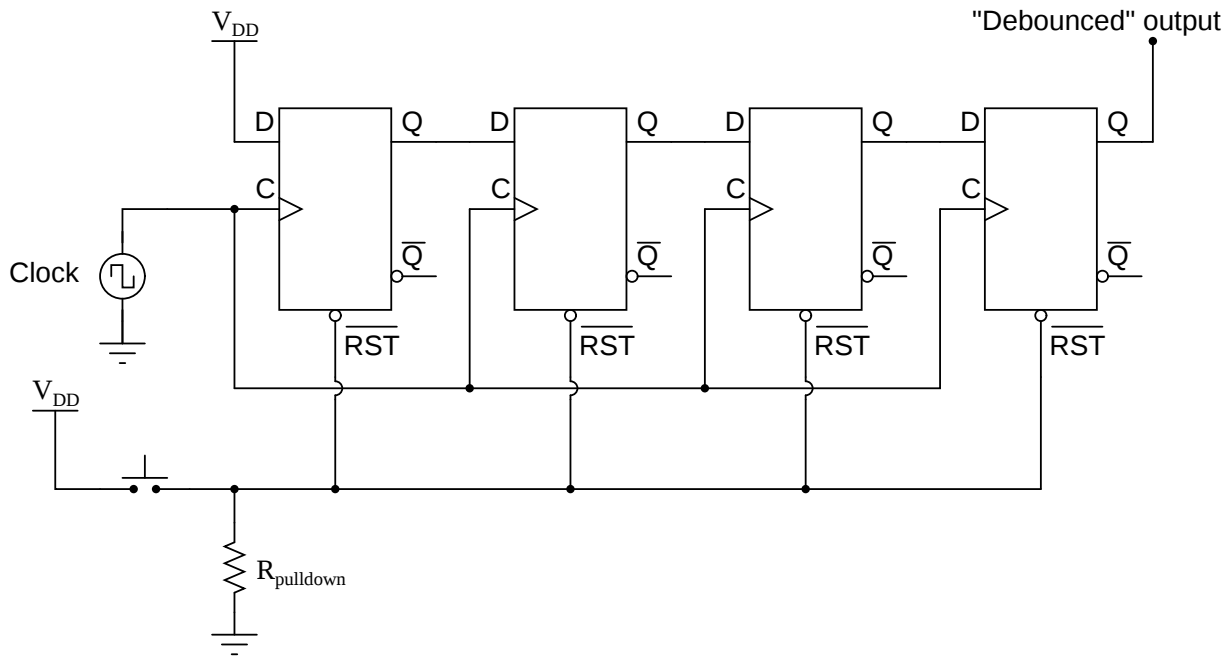
[file 01474](#)

*Switch contact bounce* is often a problem when connecting mechanical contact switches or relays to the inputs of digital semiconductor circuits. When a switch transitions from open to closed, or from closed to open, there is usually a burst of on/off pulses rather than a single, crisp, change of logic state:



Digital electronic circuits, of course, react to these pulses as though they were very rapid actuations/de-actuations of the switch. This may cause problems, especially in applications where a mechanical switch input causes a counter to increment or decrement!

To fix this problem we must properly condition the switch signal to eliminate the spurious on/off pulses. The process of doing this is called *debouncing*. There is more than one way to de-bounce a switch, but one of the more sophisticated ways uses a serial-in, serial-out shift register with an asynchronous reset (clear) input:



Explain how this circuit works to de-bounce the switch's "dirty" signal, producing a "clean" (de-bounced) signal for a subsequent digital circuit's input.

[file 02990](#)

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#### Question 17

A mechanically inclined friend of yours wishes to build an automated water fountain, where ten water jets are turned on in sequence, one at a time. Each water jet is controlled by a solenoid valve, energized by 120 volt AC line power.

Your friend understands how to wire up the solenoid valves and build all the plumbing to make the fountain work. He also understands how to interpose power to the solenoid valve coils using small relays, so a digital control circuit operating at a low DC supply voltage will be able to energize the valves. The only problem is, this friend of yours does not know how to build a circuit to do the sequencing. How do you turn on one out of ten outputs at a time, in sequence?

Another friend who is studying digital circuits has a solution to the design problem. She says you could generate a one-out-ten sequence by using a BCD counter (4 bit) driving a 4-line to 16-line decoder. A simple 555 astable multivibrator circuit could supply the necessary clock pulses, and the decoder outputs could drive the relays, and then the solenoid valves, as fast as you wanted.

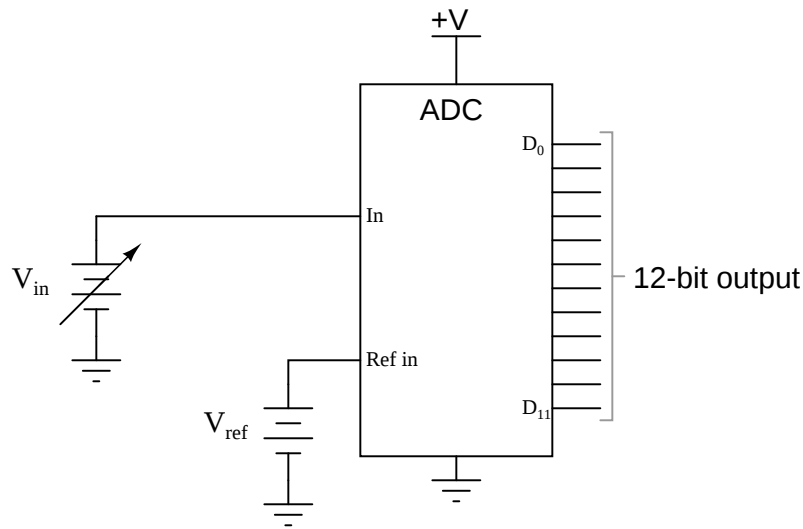
However, your instructor just recently told you about a different way to generate a one-out-of- $n$  counting sequence by using shift registers: the circuit is called a *ring counter*. A ring counter would use fewer parts than the counter/decoder idea. Explain what a ring counter is, and how it would work in this application.

[file 01473](#)

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### Question 18

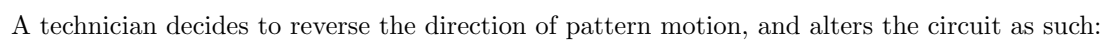
An analog-to-digital converter is a circuit that inputs an analog signal and outputs a multiple-bit binary number equivalent to that signal's amplitude:

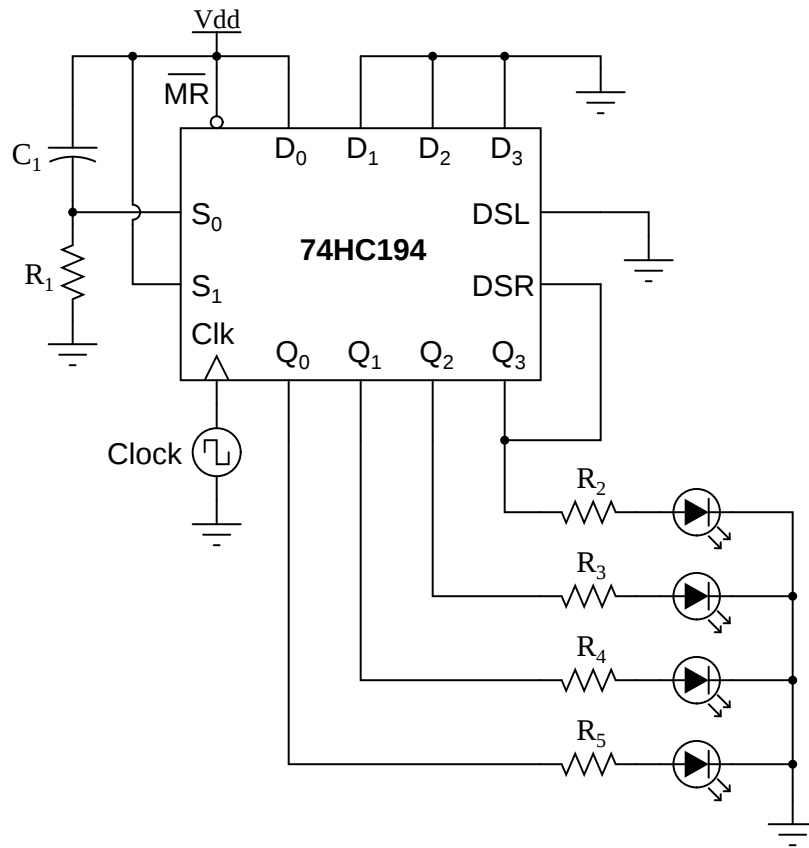


A *free-running* analog-to-digital converter is one that updates its digital output as often as it can, not waiting for any prompting from another device. If we were to connect a free-running ADC to a computer (microprocessor or microcontroller), we would need some way to sample the ADC's output at times specified by the computer, and hold that binary number long enough for the computer to register it. Otherwise, the ADC may update its output in the middle of one of the computer's "input" cycles, possibly resulting in corrupted data.

We could build such a sample-and-hold circuit out of flip-flops. What type of flip-flop would we use for this purpose, and how many would we need for the ADC circuit shown above? This circuit we would build is also known as a *shift register*. What kind of shift register inputs multiple bits of data all at once, and transfers that data to its output lines all at once, at the command of a clock pulse?

file 01465



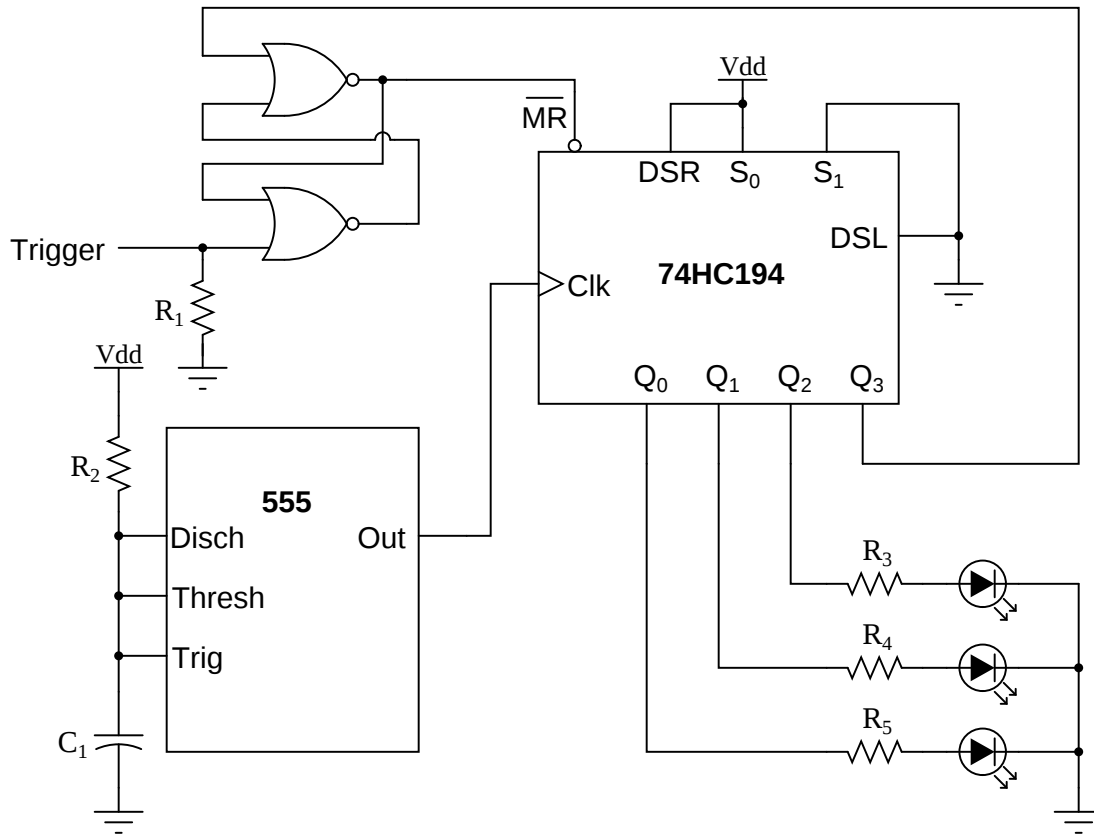


Unfortunately, this does not work as planned. Now, the bottom LED blinks once upon power-up, then all LEDs remain off. What did the technician do that was incorrect? What needs to be done to fix the problem?

file 03904

## Question 20

This shift register circuit produces a sequential light pattern reminiscent of the old Mercury Cougar tail-lights: first one LED energizes, then two LEDs energize, and then all three LEDs energize before all de-energizing and repeating the sequence. The 74HC194 shift register circuit is set to always operate in the "shift right" mode with the shift-right serial input ( $DSR$ ) tied high, the master reset ( $\overline{MR}$ ) input used to set all output lines to a low state at the end of each cycle:

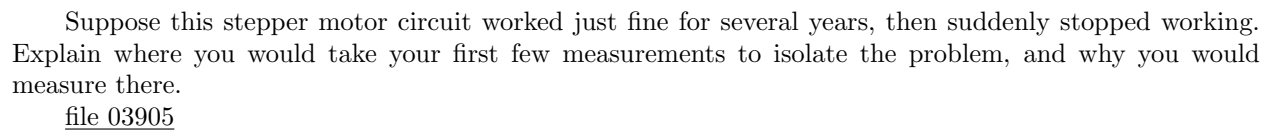


The sequential light pattern is supposed to begin whenever the "Trigger" input momentarily goes high. Unfortunately, something has failed in this circuit which is preventing any of the LEDs to come on. No blinking light sequence ensues, no matter what the state of the "Trigger" input.

Identify some likely failures in this circuit that could cause this to happen, other than a lack of power supply voltage. Explain why each of your proposed faults would cause the problem, and also identify how you would isolate each fault using test equipment.

[file 03906](#)

This shift register circuit drives the four coils of a unipolar stepper motor, one at a time, in a rotating pattern that moves at the pace of the clock. The drive transistor circuitry ( $Q_1$ ,  $Q_2$ , and resistors  $R_2$  through  $R_6$ ) are shown only for one of the four coils. The other three shift register outputs have identical drive circuits connected to the respective motor coils:



### **Don't just sit there! Build something!!**

Learning to analyze digital circuits requires much study and practice. Typically, students practice by working through lots of sample problems and checking their answers against those provided by the textbook or the instructor. While this is good, there is a much better way.

You will learn much more by actually *building and analyzing real circuits*, letting your test equipment provide the "answers" instead of a book or another person. For successful circuit-building exercises, follow these steps:

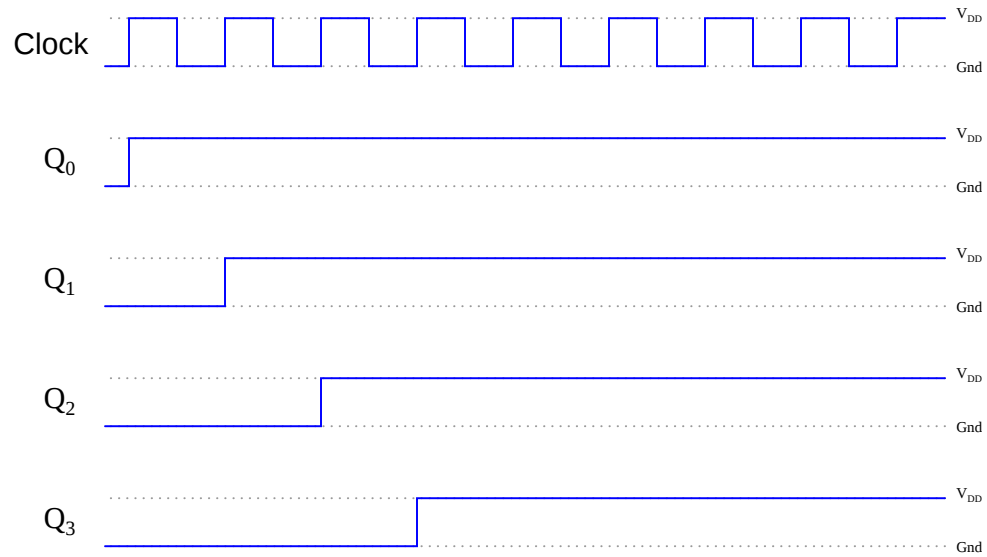
1. Draw the schematic diagram for the digital circuit to be analyzed.
2. Carefully build this circuit on a breadboard or other convenient medium.
3. Check the accuracy of the circuit's construction, following each wire to each connection point, and verifying these elements one-by-one on the diagram.
4. Analyze the circuit, determining all output logic states for given input conditions.
5. Carefully measure those logic states, to verify the accuracy of your analysis.
6. If there are any errors, carefully check your circuit's construction against the diagram, then carefully re-analyze the circuit and re-measure.

Always be sure that the power supply voltage levels are within specification for the logic circuits you plan to use. If TTL, the power supply *must* be a 5-volt regulated supply, adjusted to a value as close to 5.0 volts DC as possible.

One way you can save time and reduce the possibility of error is to begin with a very simple circuit and incrementally add components to increase its complexity after each analysis, rather than building a whole new circuit for each practice problem. Another time-saving technique is to re-use the same components in a variety of different circuit configurations. This way, you won't have to measure any component's value more than once.

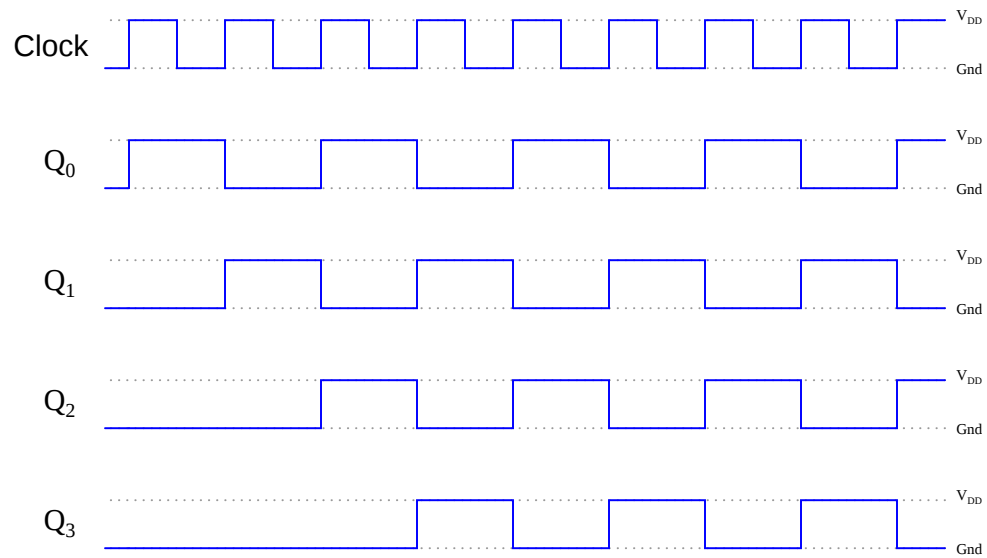
file 00805

Answer 1



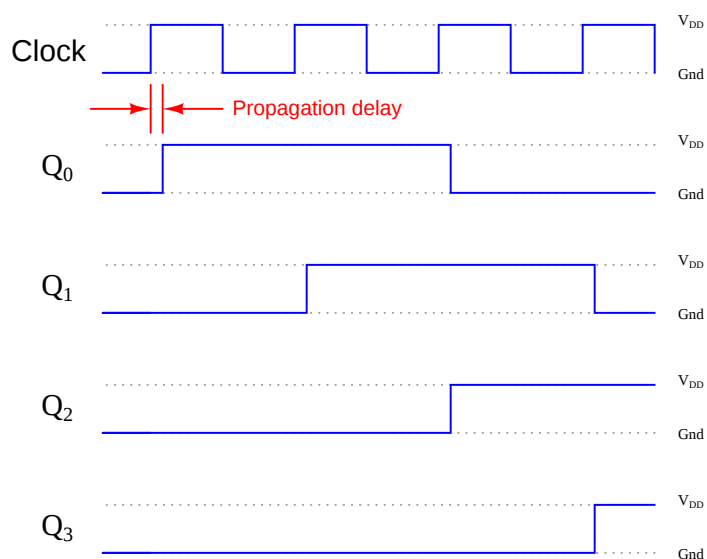
Follow-up question: explain why the "high" state at the  $D$  input of the first flip-flop does not ripple through *all* the flip-flops at the first clock pulse.

Answer 2



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Answer 3



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Answer 4

A "register" is a collection of flip-flops or latches used to store a binary number (several bits). In essence, it is a one-word *memory circuit*. A "shift register" does the same thing as a register, except that it also has the ability to *move* that binary word from one place to another.

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Answer 5

*Serial* data is transmitted along one line, one bit at a time; *parallel* data is transmitted all at once.

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Answer 6

This is a *serial-in, parallel-out* shift register analogy, with each box arriving on the conveyor belt one at a time, but leaving together as a group.

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Answer 7

This is a *parallel-in, serial-out* shift register analogy, with each box arriving on the conveyor belt one at a time, but leaving together as a group.

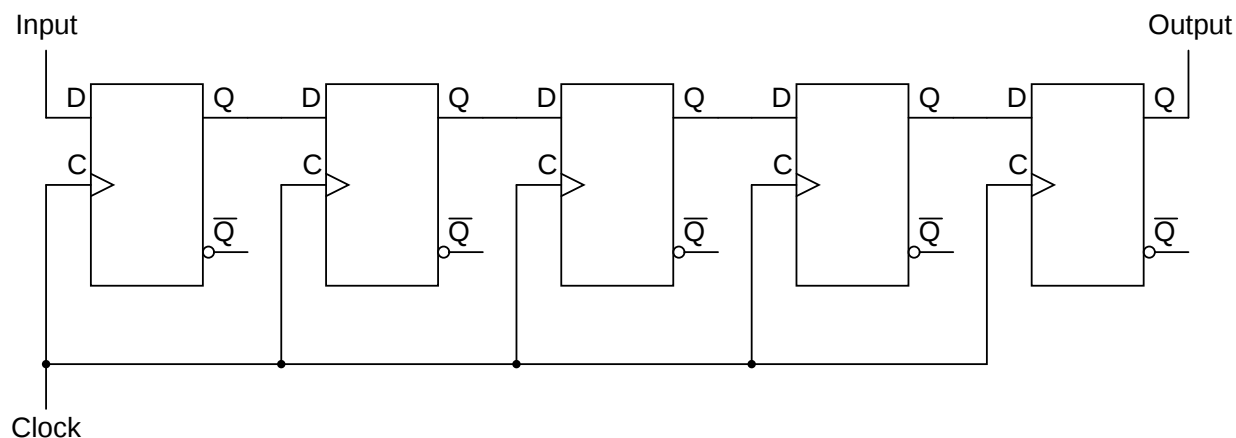
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Answer 8

This is a *serial-in, serial-out* shift register analogy, with each box arriving on the conveyor belt one at a time, and leaving one at a time as well.

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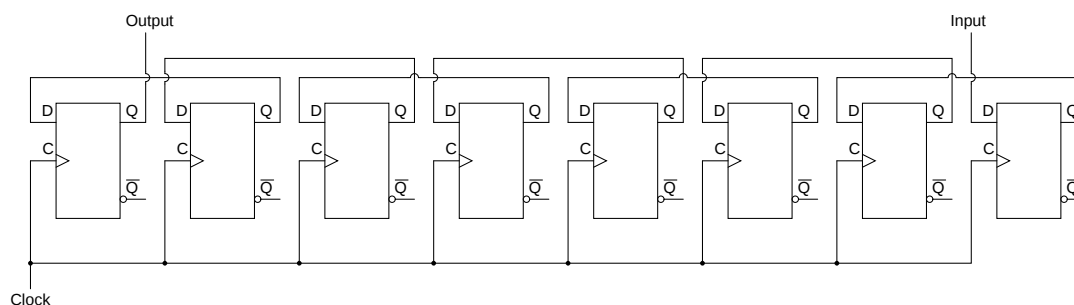
Answer 9



Follow-up question: if we wished to output data from this shift register circuit in parallel form, where would we make the connections?

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Answer 10



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Answer 11

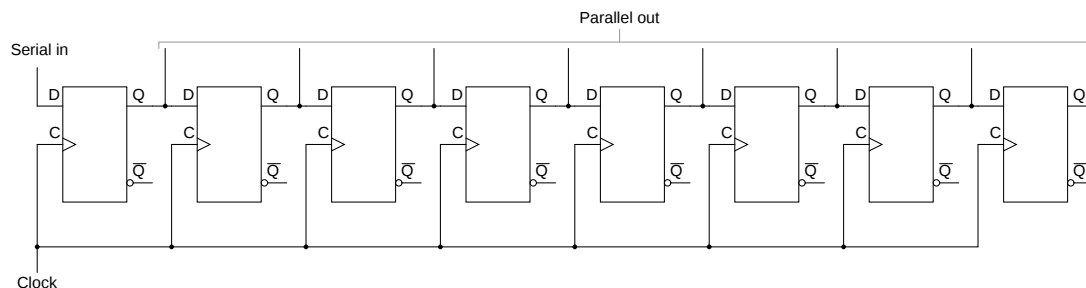
In order to provide *bidirectional* shift direction ability to a shift register circuit, you will probably have to use *steering gates* to direct the flip-flop outputs to different flip-flop inputs. I'll leave the details for you to research and explain!

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Answer 12

A *universal* shift register has the ability to input data in either serial or parallel form, as well as output data in either serial or parallel form.

Note: this answer is purposely minimal. I expect you to provide a more detailed answer during discussion, based on research of universal shift registers!



Follow-up question: if we were to actually use this circuit for serial-to-parallel data conversion, we would have to be careful how fast we clocked the shift register. Explain why.

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Answer 14

Perhaps the most direct way to provide parallel data entry is to make use of the flip-flops' asynchronous inputs.

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Answer 15

The new binary number value will be one-half (or approximately one-half) the value that it was before.

Follow-up question: how could we use the shift register to *double* the value of a binary number?

Challenge question: when we divide a binary number in two by shifting its bit positions, the resulting answer may or may not be exactly one-half the original value. Explain why this is so. Also, analyze what happens when we *multiply* a binary number by two through a process of bit-shifting. Is the resulting answer exactly twice the original value, or may it also be *approximate* as it sometimes is with division? Explain why or why not.

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Answer 16

The "de-bounced" output line will go high only when the switch signal has been continuously high for at least four clock pulses.

Follow-up question #1: which switch (input) transition is seen *immediately* at the output, a low-to-high transition or a high-to-low transition?

Follow-up question #2: does this circuit de-bounce a noisy low-to-high switch (input) transition, a noisy high-to-low switch transition, or both?

Follow-up question #3: does the pushbutton switch *source* or *sink* current in this circuit?

Challenge question: how would you go about selecting an appropriate clock frequency for this circuit?

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Answer 17

Ring counter circuits are simple enough to not require my elaboration. The only tough part of this design problem might be in achieving the initial "high" state in the first bit of the shift register upon power-up. The rest is child's play!

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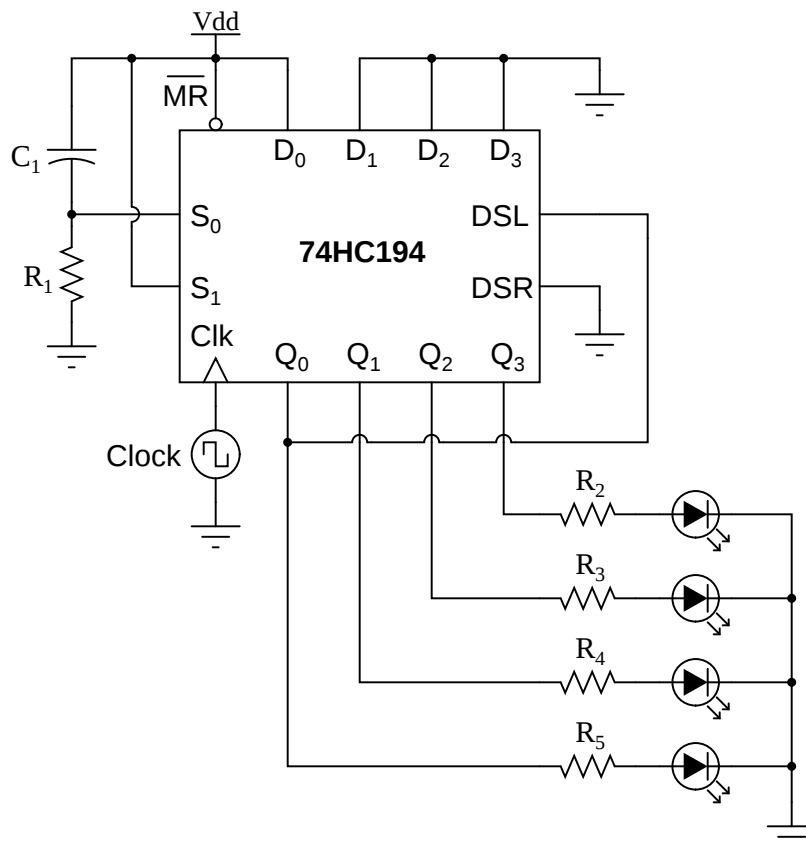
Answer 18

Use twelve D-type flip-flops to build a parallel-in/parallel-out shift register.

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Answer 19

This will fix the problem:



I will leave it to you to explain *why* this modification works!

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Answer 20

Lack of a clock signal could cause this to happen (check the output of the 555 oscillator with a logic probe or voltmeter). If the upper NOR gate output was failed low, it would also create the problem (check  $\overline{MR}$  input of the shift register for a "low" state, and compare with the NOR gate input states).

These are not the only possible failures. Identify a few more on your own!

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Answer 21

My first step would be to check for the presence of adequate DC power to both the shift register IC and the motor (transistor drive circuitry). Then, I would use a voltmeter or logic probe to check for pulsing at any one of the shift register's  $Q$  outputs. That would tell me whether the problem was with the shift register or with the power circuitry.

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Answer 22

Let the electrons themselves give you the answers to your own "practice problems"!

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Notes 1

This question reviews the principles of D-type flip-flops, timing diagrams, and serves as an introduction to shift registers.

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Notes 2

This question reviews the principles of D-type flip-flops, timing diagrams, and serves as an introduction to shift registers.

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Notes 3

This question reviews the principles of D-type flip-flops, timing diagrams, and serves as an introduction to shift registers.

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Notes 4

Registers are a good way to introduce the basic concepts of solid-state (RAM) memory technology, showing how flip-flop or latch circuits may be used as data storage devices. Be sure to ask your students where they were able to find this information, as it should be very easy for them to research!

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Notes 5

Ask your students if they have ever heard of "serial" and "parallel" ports on personal computers. If time permits, have them examine the two types of ports on the back of a PC, contrasting the number of pins used for each connector.

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Notes 6

Some analogies can be very helpful to students as they learn new concepts. I have found that conveyor belts work very well to illustrate the different types of shift register behaviors.

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Notes 7

Some analogies can be very helpful to students as they learn new concepts. I have found that conveyor belts work very well to illustrate the different types of shift register behaviors.

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Notes 8

Some analogies can be very helpful to students as they learn new concepts. I have found that conveyor belts work very well to illustrate the different types of shift register behaviors.

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Notes 9

One application for a serial-in/serial-out shift register is a precise time delay for serial-stream data.

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Notes 10

This is a very simple question to answer. I present it here primarily so that students start thinking about how to *change* the direction of shift in a shift register circuit: a prelude to bidirectional shift register circuits.

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Notes 11

I purposely avoided asking a question about schematics for a reason: it is too easy to simply look through a textbook or research a datasheet and copy a drawing. What is most important here is that students comprehend *how* bidirectionality is achieved in shift register circuits. What, exactly, is a *steering gate*, why are they used, and what in/out flip-flop connections are needed to achieve a desired shift direction.

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#### Notes 12

Regarding the point in the answer about student research, ask your students how a single shift register chip is able to perform all the different types of input/output combinations. Ask your students, for instance, how to make the 74194 act *only* as a parallel-in/parallel-out register.

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#### Notes 13

The subject of serial-to-parallel data conversion is much deeper than what is suggested by this disarmingly simple circuit. Talk with your students about the need for clock synchronization (even in "asynchronous" serial data transmission).

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#### Notes 14

During discussion, have your students draw a picture of a parallel-in/serial-out shift register circuit, or at least cite a page number reference in their textbook, so you may be sure they understand what they're talking about (and not just repeating the given answer).

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#### Notes 15

This is a really neat trick for dividing or multiplying binary numbers by powers of two. It is often used in machine-language microprocessor programming, due to its simplicity of execution.

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#### Notes 16

Some students may need to see a pulse diagram for this circuit before they fully grasp how it functions. If so, have students come up to the board in the front of the room and work through an analysis of it rather than doing it yourself.

Not only does this question review shift register operation, but it also reviews the problem of switch contact bounce and showcases a practical solution for it. Incidentally, this question provides a good excuse for a hands-on demonstration of switch bounce using the switch/pulldown circuit first shown and a digital storage oscilloscope to capture the switching action. Students are likely to be surprised by just how "dirty" the switch signal is!

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#### Notes 17

Actually, this question would make a great class project for your students to build. Not just the ring counter itself, but all the power supply and power control circuitry as well. I've actually had a student team build a programmable ornamental fountain before, using a microcontroller. We entered it as a piece of kinetic artwork in a campus art show, and it was a hit (especially for young children playing in the water jets on a hot day). It was a lot of fun and the students learned a lot!

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#### Notes 18

This type of shift register is immensely useful for sample-and-hold applications such as this.

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#### Notes 19

Students will probably need to consult a datasheet for the 74HC194 in order to figure out what is going on here. If they have not taken the initiative to do so, encourage them to both now and later! Datasheets are an invaluable source of information when it comes to integrated circuit behavior and the conditions necessary to make them do what you want them to do.

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#### Notes 20

Students will find a datasheet for the 74HC194 helpful in figuring out how this circuit is supposed to work.

This is a good question to discuss with your students, as it helps them understand how to "divide and conquer" a malfunctioning system.

It has been my experience that students require much practice with circuit analysis to become proficient. To this end, instructors usually provide their students with lots of practice problems to work through, and provide answers for students to check their work against. While this approach makes students proficient in circuit theory, it fails to fully educate them.

Students don't just need mathematical practice. They also need real, hands-on practice building circuits and using test equipment. So, I suggest the following alternative approach: students should *build* their own "practice problems" with real components, and try to predict the various logic states. This way, the digital theory "comes alive," and students gain practical proficiency they wouldn't gain merely by solving Boolean equations or simplifying Karnaugh maps.

Another reason for following this method of practice is to teach students *scientific method*: the process of testing a hypothesis (in this case, logic state predictions) by performing a real experiment. Students will also develop real troubleshooting skills as they occasionally make circuit construction errors.

Spend a few moments of time with your class to review some of the "rules" for building circuits before they begin. Discuss these issues with your students in the same Socratic manner you would normally discuss the worksheet questions, rather than simply telling them what they should and should not do. I never cease to be amazed at how poorly students grasp instructions when presented in a typical lecture (instructor monologue) format!

I highly recommend CMOS logic circuitry for at-home experiments, where students may not have access to a 5-volt regulated power supply. Modern CMOS circuitry is far more rugged with regard to static discharge than the first CMOS circuits, so fears of students harming these devices by not having a "proper" laboratory set up at home are largely unfounded.

A note to those instructors who may complain about the "wasted" time required to have students build real circuits instead of just mathematically analyzing theoretical circuits:

*What is the purpose of students taking your course?*

If your students will be working with real circuits, then they should learn on real circuits whenever possible. If your goal is to educate theoretical physicists, then stick with abstract analysis, by all means! But most of us plan for our students to do something in the real world with the education we give them. The "wasted" time spent building real circuits will pay huge dividends when it comes time for them to apply their knowledge to practical problems.

Furthermore, having students build their own practice problems teaches them how to perform *primary research*, thus empowering them to continue their electrical/electronics education autonomously.

In most sciences, realistic experiments are much more difficult and expensive to set up than electrical circuits. Nuclear physics, biology, geology, and chemistry professors would just love to be able to have their students apply advanced mathematics to real experiments posing no safety hazard and costing less than a textbook. They can't, but you can. Exploit the convenience inherent to your science, and *get those students of yours practicing their math on lots of real circuits!*